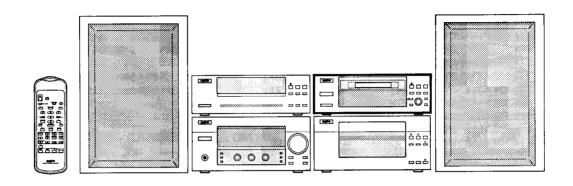


FILE NO.

Service Manual

Separate Mini Component System MD Deck

MDG-007 (UK) (XE)



Specifications

Specifications subject to change without notice.

PRODUCT CODE No. 137 088 00 (UK) 137 088 01 (XE)

LASER BAEM SAFETY PRECAUTION

· Pick-up that emits a laser beam is used in this CD player section.

CAUTION:

USE OF CONTROLS OR ADJUSTMENTS OR PERFORMANCE OF PROCEDURES OTHER THAN THOSE SPECIFIED HEREIN MAY RESULT IN HAZARDOUS RADIATION EXPOSURE

LASER OUTPUT........... 0.6 mW Max. (CW) WAVELENGTH 790 nm

(MD DECK)

CAUTION - INVISIBLE LASER RADIATION WHEN OPEN AND INTERLOCKS DEFEATED. AVOID EXPOSURE TO BEAM.

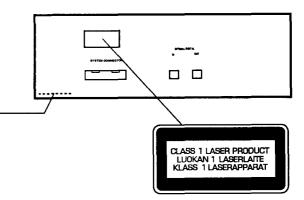
ADVARSEL – USYNLIG LASER STRÅLING VED ÅBNING, NÅR SIKKERHEDSAFBRYDERE ER UDE AF FUNKTION, UNDGÅ UDS ÆTTELSE FOR STRÅLING.

VARNING – OSYNLIG LASER STRÅLNING NÄR DENNA DEL ÄR ÖPPNAD OCH SPÄRR ÄR URKOPPLAD. STRÅLEN ÄR FARLIG.

VORSICHT – UNSICHTBARE LASERSTRAHLUNG TRITT AUS, WENN DECKEL GEÖFFNET UND WENN SICHERHEITSVERRIEGELUNG ÜBERBRÜCKT IST. NICHT, DEM STRAHL AUSSETZEN.

VARO – AVATTAESSA JA SUOJALUKITUS OHITETTAESSA OLET ALTTIINA NÄKYMÄTTÖMÄLLE LASERSÄTEILYLLE. ÄLÄ KATSO SÄTEESEEN.





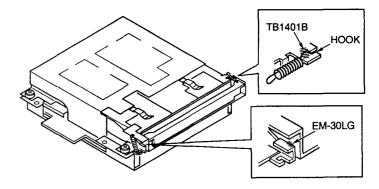
REPLACEMENT AND LUBRICATION OF THE MD DOOR

Remove it from, and is a method MD door

- A point of a coil(Front right side) is pinched with tweezers, and it istaken off a hole of MD door.
 A spring turns then, and seem not to be exhausted.
- 2. A ditch of a door is to be pushed from face, and MD door is taken off an axis.
- 3. MD door axis is avoided.

MD door installation method

- 1. Put molykote (EM-30LG) on door contact department.
- Fit turns an axis into ditch 2 places of MD door after installing door shaft, and install it with care in installation direction.
- 3. Pinch a point of coil spring with tweezers, and hang it on a hole of a door.



EXPLODED VIEW (CABINET, CHASSIS & MD MECHANISM) Y04 -Y04 Y04 -Y04 -Y04 ---Y06 Y06 Y06-MM02 MM00 -MM01-`MM03 Y08 Y07 76 73] 53 Y09 Y03 Y03 Y03 Y02 (75) -Y05 -Y02 51 Y01-

14

-Y01

-Y01

PRODUCT SAFETY NOTICE

EACH PRECAUTION IN THIS MANUAL SHOULD BE FOLLOWED DURING SERVICING. COMPONENTS IDENTIFIED WITH THE IEC SYMBOL Δ IN THE PARTS LIST AND THE SCHEMATIC DIAGRAM DESIGNATED COMPONENTS IN WHICH SAFETY CAN BE OF SPECIAL SIGNIFICANCE. WHEN REPLACING A COMPONENT IDENTIFIED BY Δ , USE ONLY THE REPLACEMENT PARTS DESIGNATED, OR PARTS WITH THE SAME RATINGS OF RESISTANCE, WATTAGE OR VOLTAGE THAT ARE DESIGNATED IN THE PARTS LIST IN THIS MANUAL. LEAKAGE-CURRENT OR RESISTANCE MEASUREMENTS MUST BE MADE TO DETERMINE THAT EXPOSED PARTS ARE ACCEPTABLY INSULATED FROM THE SUPPLY CIRCUIT BEFORE RETURNING THE PRODUCT TO THE CUSTOMER.

CAUTION: Regular type resistors and capacitors are not listed. To know those values, refer to the schematic diagram.

Regular type resistors are less than 1/4 W carbon type and 0 ohm chip resistors.

Regular type capacitors are less than 50 V and less than 1000 μF type of Ceramic type and Electrical type.

N.S.P: Not available as service parts.

PACKING	& ACCESSO	RIES	ELECTRICA	AL PARTS	
REF.NO.	PART NO.	DESCRIPTION	REF.NO.	PART NO.	DESCRIPTION
	614 305 2135	CARTON CASE,INNER (UK)	51	645 036 9650	FLEXIBLE FLAT CABLE,
		CARTON CASE,INNER (XE)	•		MECHA-FRONT PWB
		CUSHION, FRONT, FRONT	52	645 037 3190	FLEXIBLE FLAT CABLE,
		CUSHION, BACK, BACK	02	040 007 0100	MECHA-POWER SUPPLY PWB
		POLY SHEET-0750X0400*NC,SET (UK)	53	645 034 8020	TRANS,POWER
		POLY SHEET-0750X0400*NC,SET (XE)	54	614 129 9082	
		CABLE,OPTICAL	04	014 129 9002	Lod
		•			
		INSTRUCTION MANUAL (UK) INSTRUCTION MANUAL (XE)			
		INSTRUCTION MANUAL, GREEK (XE)	MD SW P.W	V.BOARD AS	SSY
	014 307 0003	INSTRUCTION MANUAL, GREEK (XE)	REF.NO.	PART NO.	DESCRIPTION
			71	614 303 3851	ASSY,PWB,MD SW(Only Initial)
CABINET	& CHASSIS		CN543	614 035 4911	
REF.NO.	PART NO.	DESCRIPTION	or	614 237 9752	
1	614 302 8468	ASSY,CABINET,FRONT	S5430		SWITCH,PUSH 1P-1T
2		ASSY,DEC MD	or	614 220 5471	
3		KNOB,ROTARY,JOG	or		SWITCH,TACT
	614 302 4361		S5431		SWITCH,PUSH 1P-1T
4		BUTTON,EJECT,OPEN/CLOSE	or	614 220 5471	•
5		DEC, WINDOW LED, PLAY COMPUREC	or		SWITCH,TACT
6	614 302 4255		Oi	014 240 1002	5WITOH, 1A01
7		BUTTON,PLAY,PLAY/PAUSE			
8		HOLDER,LED,COMPU REC			
9		REFLECTOR, LED, LEFT	MD FRONT	P.W.BOARI	DASSY
10		REFLECTOR, LED, RIGHT	REF.NO.	PART NO.	DESCRIPTION
11		ASSY,CABINET,AFTER BENDING	72		ASSY,PWB,MD FRONT (Only Initial)
12		DEC,SHEET,LED,LEFT	CN530	614 303 1550	
13		DEC,SHEET,LED,RIGHT	CN531	614 302 4231	
14	614 302 8956	ASSY,CABINET,BOTTOM	CN532		SOCKET,FPC 23P
15	614 302 4385	PANEL,REAR,REAR (UK)	CN532		SOCKET, I TO 251 SOCKET, DIP 2P
15	614 304 0057	PANEL,REAR,REAR (XE)		614 237 9752	
16	614 302 4378	MOUNTING,MD,MECHA	or CN534		
	614 274 8190	LABEL,SAFETY,LASER		614 035 4911	
			or CN535	614 237 9752	
				614 035 4911	•
EIVING DA	DTC		or DE201	614 237 9752	
FIXING PA			D5301		LED SLP-3118B-51HAB-T1
REF.NO.	PART NO.	DESCRIPTION	D5302		LED SLP-9118C-51H-S-T1
Y01	411 021 3503	SCR S-TPG BIN 3X10,	FL501		FLOURESCENT TUBE
		FRONT-BOTTOM FIX	HL530		HOLDER,FL,HOLDER-FL
Y02	411 021 3701	SCR S-TPG BIN 3X10,	IC531		IC M38197MAA-625FP
		REAR-ELECT PART	IC532		IC TC74ACT32FT
Y03	411 021 3503	SCR S-TPG BIN 3X10,FRONT PWB FIX	L5961		INDUCTOR,10U K
Y04		SCR S-TPG BIN 3X10,CABINET	or		INDUCTOR,10U K
Y05	411 021 3701	SCR S-TPG BIN 3X10,BOTTOM-REAR	L5962		INDUCTOR,10U K
Y06	411 023 4003	SCR S-TPG PAN 3X10,MD MECHA	or	645 031 7835	•
Y07	411 021 3503	SCR S-TPG BIN 3X10,	Q5301		TR DTC114TS
		MOUNTING-BOTTOM	or		TR KRC111M
Y08	411 020 9902	SCR S-TPG BRZ+FLG 3X8,TRANS	Q5302		TR DTC114TS
Y09	411 021 3503	SCR S-TPG BIN 3X10,MAIN PWB	or	405 109 9402	TR KRC111M
		·	Q5303	405 143 8706	TR KTC3199-GR
			or	405 017 9600	TR 2SC3330-T
			or	405 017 9709	TR 2SC3330-U
			or	405 011 8500	TR 2SC1740S-R
			or	405 011 8609	TR 2SC1740S-S

PΑ	R	TS	LIST
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1 71110					
REF.NO.	PART NO.	DESCRIPTION	MD POWI	ER SUPPLY P.	.W.BOARD ASSY
Q5304		TR DTC144ES	REF.NO.	PART NO.	DESCRIPTION
or		TR KRC104M	75	614 302 6808	ASSY,PWB,MD POWER SUPPLY
Q5305	405 000 2205	TR DTA144ËS			(Only Initial)
or	405 146 1308	TR KRA104M	C5021	403 057 0007	POLYESTER 0.01U J 50V
Q5330	405 075 8102	TR DTA143ZS	C5905		ELECT 4700U M 25V
or		TR KRA106M			
			CN500	645 005 8134	*
RA531	614 218 0525		CN501	645 006 0861	PLUG,7P
S5301	645 034 8037	SWITCH,ROTARY(ENCODER)	CN502	614 239 5455	SOCKET
S5302	645 006 5958	SWITCH,PUSH 1P-1T	CN503	645 035 5769	SOCKET,FPC 26P
or	614 220 5471	SWITCH, TACT	CN572		PHOTO COUPLE GP1F38R
		SWITCH,TACT			
or			CN573		PHOTO COUPLE TOTX140
S5304		SWITCH,PUSH 1P-1T	or		PC PHOTO COUPLE GP1F38T
or	614 220 5471	SWITCH,TACT	CN591	614 020 6548	SOCKET,2P
or	614 240 1002	SWITCH,TACT	or	614 223 9209	SOCKET
S5305	645 006 5958	SWITCH,PUSH 1P-1T	CN592	614 020 6586	SOCKET.6P
or		SWITCH, TACT	D5901	△ 407 148 6701	•
or		SWITCH, TACT	D5902	▲ 407 148 6701	
S5306		SWITCH,PUSH 1P-1T	D5903	▲ 407 148 6701	
or	614 220 5471	SWITCH,TACT	D5904	△ 407 148 6701	DIODE 1A3-I
or	614 240 1002	SWITCH,TACT	D5931	∆ 407 148 6701	DIODE 1A3-I
S5308	645 006 5958	SWITCH,PUSH 1P-1T	D5932		ZENER DIODE MTZJ36B
	614 220 5471				
or		•	D5933		ZENER DIODE MTZJ6.8B
or		SWITCH,TACT	HS501	614 264 0159	
S5309	645 006 5958	SWITCH,PUSH 1P-1T	IC503	409 052 1407	IC TC74HCU04P
or	614 220 5471	SWITCH,TACT	IC571	409 384 3506	IC BA3314F
or	614 240 1002	SWITCH, TACT	IC572	409 426 1804	
S5310	645 006 5958	•			
		•	IC573		IC BU4066BCF
or	614 220 5471		IC592		IC NJM2930L85
or		SWITCH,TACT	IC593	409 382 0309	IC LA5620
S5311	645 006 5958	SWITCH,PUSH 1P-1T	L5701	645 001 4581	INDUCTOR,100U K
or	614 220 5471		or		INDUCTOR,100U K
or		SWITCH,TACT	L5801		INDUCTOR,100U K
S5312		SWITCH, PUSH 1P-1T	or		INDUCTOR,100U K
or	614 220 5471		L5901		INDUCTOR,10U K
or	614 240 1002	SWITCH,TACT	or	645 031 7835	INDUCTOR,10U K
S5313	645 006 5958	SWITCH,PUSH 1P-1T	L5902	645 001 4581	INDUCTOR,100U K
or		SWITCH, TACT	or		INDUCTOR,100U K
or or		SWITCH,TACT			INDUCTOR,1.1UH
		•	L5903		
X5301	645 027 5470	OSC,CERAMIC 8MHZ	L5904		INDUCTOR,1.1UH
			L5905	645 006 3602	INDUCTOR,1.1UH
			L5951	645 001 5519	INDUCTOR,47U K
MDIEDO	P.W.BOARD	VSSV	LUG50	645 023 8987	FIXER,FIX_WIRE
			LUG51		FIXER,FIX_WIRE
REF.NO.		DESCRIPTION			
73	614 303 3875	ASSY,PWB,MD LED2 (Only Initial)	PR591		PROTECTOR, 0.125A 125V
CN545	614 035 4911	SOCKET,DIP 2P	Q5340		TR DTC144ES
or	614 237 9752		or	405 146 1209	TR KRC104M
			Q5341	405 000 2205	TR DTA144ES
D5450	407 212 8907	LED HLMP-BB01-J0B00	or		TR KRA104M
					TR DTC144ES
			Q5342		
MD LED4	D W DOADD	ACCV	or		TR KRC104M
	P.W.BOARD		Q5343	405 000 2205	TR DTA144ES
REF.NO.	PART NO.	DESCRIPTION	or	405 146 1308	TR KRA104M
74	614 303 3868	ASSY,PWB,MD LED1 (Only Initial)	Q5710	405 000 3400	TR DTC114TS
CN544	614 035 4911	SOCKET,DIP 2P	or		TR KRC111M
or	614 237 9752				
		LED HLMP-BB01-J0B00	Q5810		TR DTC114TS
D5440	407 212 8907	FED UFINIS-PRO1-20000	or	405 109 9402	TR KRC111M
			Q5901	405 000 2205	TR DTA144ES
			or	405 146 1308	TR KRA104M
			Q5902		TR DTC144ES
			or		TR KRC104M
			Q5903		TR KTA1266-Y
			or		TR KTA1266-GR
			or	405 004 4502	TR 2SA608-F-NP
			or		TR 2SA608-G-NP
			Q5904		TR KTC3199-GR
			or		TR 2SC3330-T
			or		TR 2SC3330-U
			or		TR 2SC1740S-R
			or	405 011 8609	TR 2SC1740S-S

PARTS LIST

REF.NO.	PART NO.	DESCRIPTION
Q5905	405 007 3106	TR 2SB544-F-MP
or	405 007 2901	TR 2SB544-E-MP
Q5931	405 141 3505	TR KTA1266-Y
or	405 141 3406	TR KTA1266-GR
or	405 004 4502	TR 2SA608-F-NP
or	405 004 5004	TR 2SA608-G-NP
SA001	411 021 3503	SCR S-TPG BIN 3X10,FOR IC593

MD PT-PRE P.W.BOARD ASSY										
REF.NO.	PART NO.	DESCRIPTION								
76	614 303 3882	ASSY,PWB,MD PT-PRE (Only Initial)								
CN596	614 020 6548	SOCKET,2P								
or	614 223 9209	SOCKET								
L5960	614 213 5761	INDUCTOR, FERITE								

MD PT-SEC P.W.BOARD ASSY

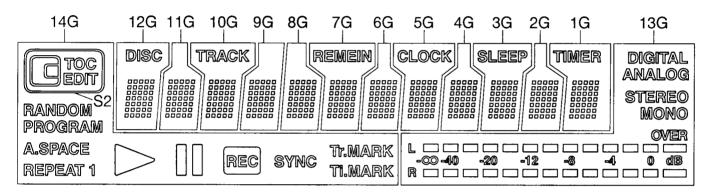
REF.NO.	PART NO.	DESCRIPTION
77	614 302 6815	ASSY,PWB,MD PT-SEC (Only Initial)

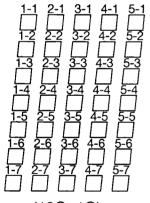
CN599 614 020 6586 SOCKET,6P △ 645 014 2550 PROTECTOR, 2A 125V PR599

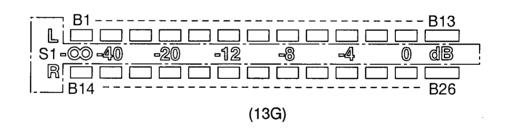
MD MECHANISM(MDG007/SH...Only Initial)

REF.NO.	PART NO.	DESCRIPTION
MM00	614 304 8350	ASSY,MECHA,MDG007/SH,MD MECHA
MM01	614 303 8344	ASSY,DOOR,MD DOOR W/PAINTING
MM02	614 303 6609	SHAFT,DOOR,MD DOOR SHAFT
MM03	614 303 6616	SPRING,DOOR,MD DOOR RETURN

FL DISPLAY & DESCRIPTION







(12G~1G)

PIN CONNECTION

PIN NO.	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41
CONNECTION	F2	F2	NP	NP	14G	13G	12G	11G	10G	9G	8G	7G	6G	5G	4G	3G	2G	1G	NC	NC
PIN NO.	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21
CONNECTION	P1	P2	P3	P4	P5	P6	P7	P8	P9	P10	P11	P12	P13	P14	P15	P16	P17	P18	P19	P20
PIN NO.	20	19	18	_17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
CONNECTION	P21	P22	P23	P24	P25	P26	P27	P28	P29	P30	P31	P32	P33	P34	P35	P36	NP	NP	F1	F1

NOTE: 1) F1,F2 Filament

NP No pin

DL Datum Line

1G~14G Grid

NC No connection

FL DISPLAY & DESCRIPTION -

ANODE CONNECTION

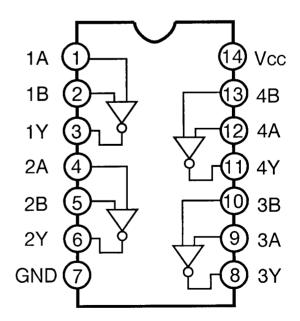
	14G	13G	12G	11G	10G	9G	8G	7G	6G	5G	4G	3G	2G	1G
P1		МОИО	DISC		TRACK			REMAIN		CLOCK		SLEEP		
P2	凹		1-1	1-1	1-1	1-1	1-1	1-1	1-1	1-1	1-1	1-1	1-1	1-1
P3	TOC	B1	2-1	2-1	2-1	2-1	2-1	2-1	2-1	2-1	2-1	2-1	2-1	2-1
P4	EDIT	В8	3-1	3-1	3-1	3-1	3-1	3-1	3-1	3-1	3-1	3-1	3-1	3-1
P5	S2	B14	4-1	4-1	4-1	4-1	4-1	4-1	4-1	4-1	4-1	4-1	4-1	4-1
P6	RANDOM	B21	5-1	5-1	5-1	5-1	5-1	5-1	5-1	5-1	5-1	5-1	5-1	5-1
P7	RPROGRAM		1-2	1-2	1-2	1-2	1-2	1-2	1-2	1-2	1-2	1-2	1-2	1-2
P8	A.SPACE	B21	2-2	2-2	2-2	2-2	2-2	2-2	2-2	2-2	2-2	2-2	2-2	2-2
P9	REPEAT	В9	3-2	3-2	3-2	3-2	3-2	3-2	3-2	3-2	3-2	3-2	3-2	3-2
P10	1	B15	4-2	4-2	4-2	4-2	4-2	4-2	4-2	4-2	4-2	4-2	4-2	4-2
P11	Δ	B22	5-2	5-2	5-2	5-2	5-2	5-2	5-2	5-2	5-2	5-2	5-2	5-2
P12	00		1-3	1-3	1-3	1-3	1-3	1-3	1-3	1-3	1-3	1-3	1-3	1-3
P13	REC	В3	2-3	2-3	2-3	2-3	2-3	2-3	2-3	2-3	2-3	2-3	2-3	2-3
P14	SYNC	B10	3-3	3-3	3-3	3-3	3-3	3-3	3-3	3-3	3-3	3-3	3-3	3-3
P15	TrMARK	B16	4-3	4-3	4-3	4-3	4-3	4-3	4-3	4-3	4-3	4-3	4-3	4-3
P16	TIMARK	B23	5-3	5-3	5-3	5-3	5-3	5-3	5-3	5-3	5-3	5-3	5-3	5-3
P17			1-4	1-4	1-4	1-4	1-4	1-4	1-4	1-4	1-4	1-4	1-4	1-4
P18		B4	2-4	2-4	2-4	2-4	2-4	2-4	2-4	2-4	2-4	2-4	2-4	2-4
P19		B11	3-4	3-4	3-4	3-4	3-4	3-4	3-4	3-4	3-4	3-4	3-4	3-4
P20	_	B17	4-4	4-4	4-4	4-4	4-4	4-4	4-4	4-4	4-4	4-4	4-4	4-4
P21		B24	5-4	5-4	5-4	5-4	5-4	5-4	5-4	5-4	5-4	5-4	5-4	5-4
P22		\$1	1-5	1-5	1-5	1-5	1-5	1-5	1-5	1-5	1-5	1-5	1-5_	1-5
P23		B5	2-5	2-5	2-5	2-5	2-5	2-5	2-5	2-5	2-5	2-5	2-5	2-5
P24		B12	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5
P25		B18	4-5	4-5	4-5	4-5	4-5	4-5	4-5	4-5	4-5	4-5	4-5	4-5
P26		B25	5-5	5-5	5-5	5-5	5-5	5-5	5-5	5-5	5-5	5-5	5-5	5 -5
P27		OVER	1-6	1-6	1-6	1-6	1-6	1-6	1-6	1-6	1-6	1-6	1-6	1-6
P28		В6	2-6	2-6	2-6	2-6	2-6	2-6	2-6	2-6	2-6	2-6	2-6	2-6
P29		B13	3-6	3-6	3-6	3-6	3-6	3-6	3-6	3-6	3-6	3-6	3-6	3-6
P30		B19	4-6	4-6	4-6	4-6	4-6	4-6	4-6	4-6	4-6	4-6	4-6	4-6
P31		B26	5-6	5-6	5-6	5-6	5-6	5-6	5-6	5-6	5-6	5-6	5-6	5 -6
P32		DIGITAL	1-7	1-7	1-7	1-7	1-7	1-7	1-7	1-7	1-7	1-7	1-7	1-7
P33		В7	2-7	2-7	2-7	2-7	2-7	2-7	2-7	2-7	2-7	2-7	2-7	2-7
P34		ANALOG	3-7	3-7	3-7	3-7	3-7	3-7	3-7	3-7	3-7	3-7	3-7	3-7
P35		B20	4-7	4-7	4-7	4-7	4-7	4-7	4-7	4-7	4-7	4-7	4-7	4-7
P36		STEREO	5-7	5-7	5-7	5-7	5-7	5-7	5-7	5-7	5-7	5-7	5-7	5-7

IC BLOCK DIAGRAM & DESCRIPTION

IC503 TC74HCU04 (HEX INVERTER)

1A 1 14 Vcc 1Y 2 13 6A 2A 3 12 6Y 2Y 4 11 5A 3A 5 10 5Y 3Y 6 9 4A GND 7 8 4Y

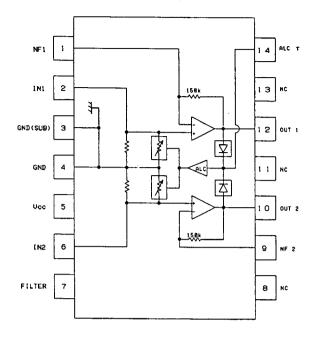
IC532 TC74ACT32FT (Serial EEPROM)

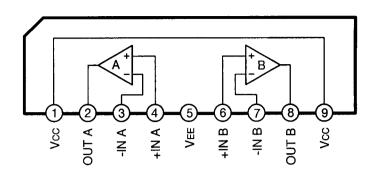


IC BLOCK DIAGRAM & DESCRIPTION

IC571 BA3314F (Dula Preamplifier with ALC Detector)

IC572 KIA4558S (Dula Low Noise Operational Amp.)

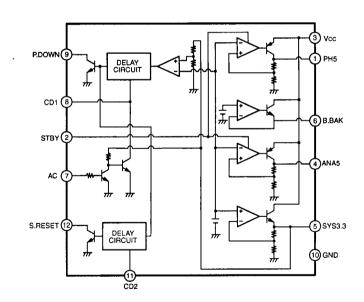


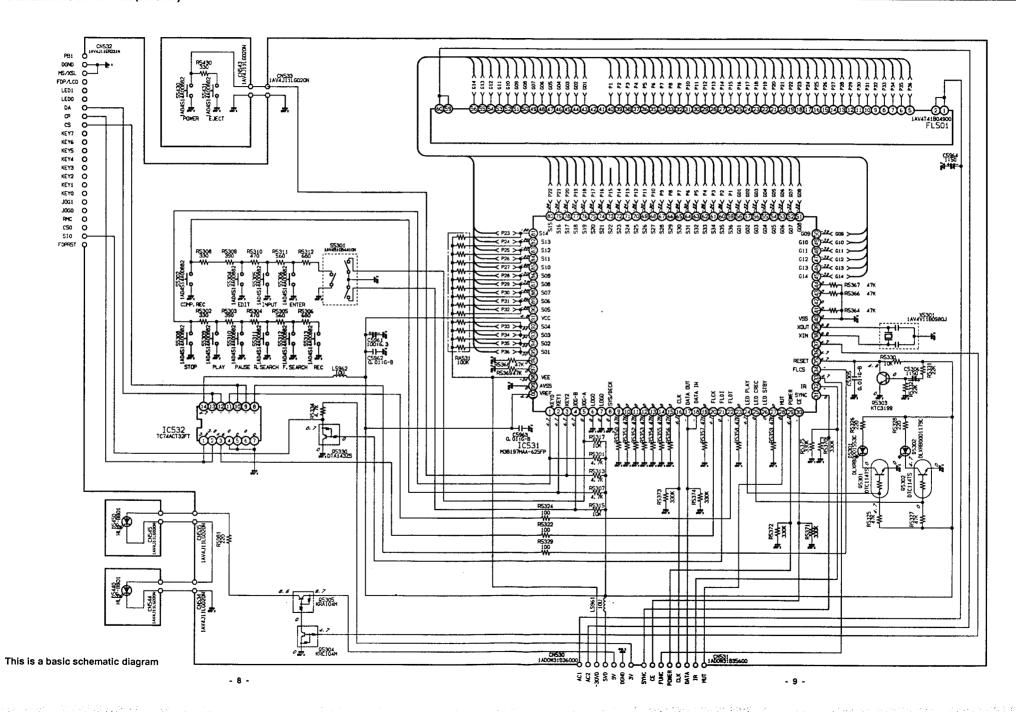


IC573 BU4066BCF (Quad Analog Switch)

I/O₁ Vcc DUT/IN IN/OUT **SWA** 13 O/I1 C_1 SWD DUT/IN IN/OUT 12) O/l2 C4 11) 1/02 1/04 N/OUT OUT/IN 5 **SWB** C2 10 **O/l**4 9 6 Сз O/l3 SWC N/OUT OUT/IN Vss I/O3

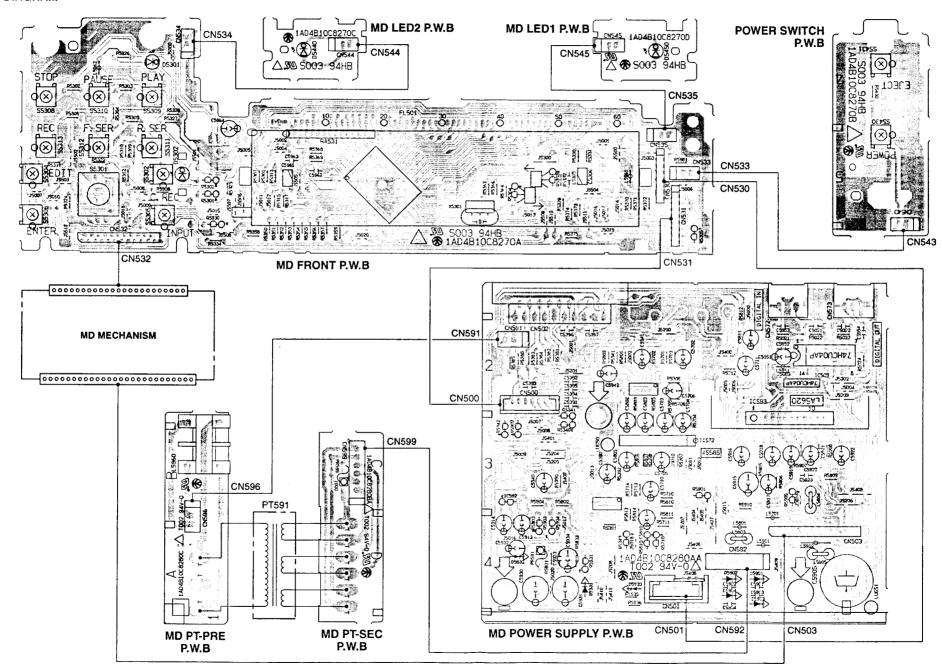
IC593 LA5620 (Multi Voltage System Regulator)



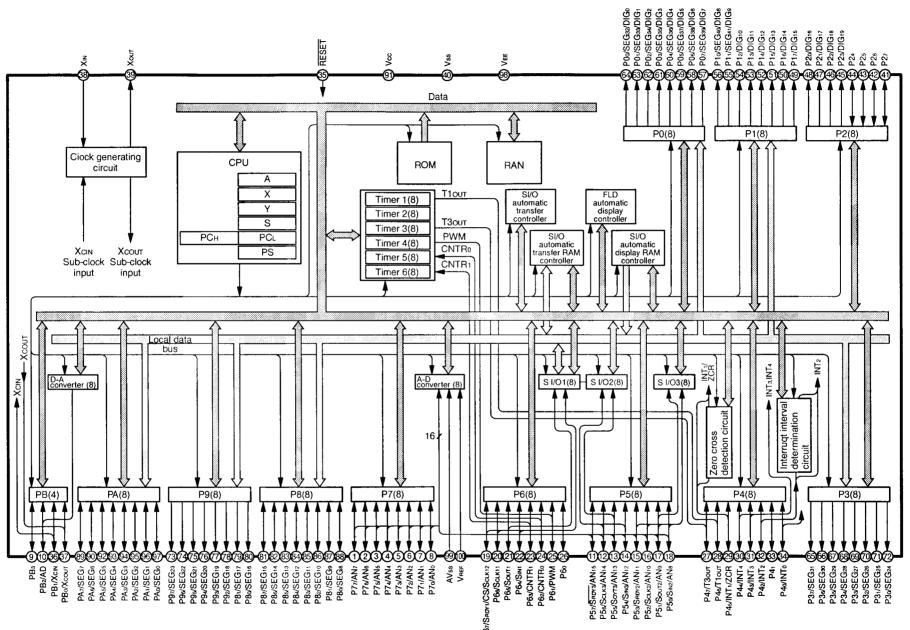


This is a basic schematic diagram

circuit before returning the product to the customer.



IC531 M38197MAA-625FP (Single Chip 8-bit C Mos)



IC BLOCK DIAGRAM & DESCRIPTION

Pin	Name	Function
1	P7 ₇ /AN ₇	
2	P7 ₆ /AN ₆	
3	P7 ₆ /AN ₆	8-bit C MOS I/O port with the same function
4	P74/AN4	as ports P24 - P27
5	P7 ₃ /AN ₃	C MOS compatible input level
6	P7 ₂ /AN ₂	C MOS 3-state output
7	P7,/AN,	O MOO 3-state output
8	P7./AN	
H	F70/AN0	
9	ne.	4-bit C MOS I/O port with the same function as ports P2, - P2,
	PB₃ PB₂/DA	C MOS compatible input level
10	F D ₂ / D A	C MOS 3-state output
11	P57/SRDY3/AN15	,
12	P5 ₆ /S _{CLK3} /AN ₁₄	
13	P5 ₆ /S _{OUT3} /AN ₁₃	
14	P5 ₄ /S _{IN3} /AN ₁₂	
15		9 bit C MOS I/O part with the same function
	P5 ₃ /S _{RDY2} /AN ₁₁	8-bit C MOS I/O port with the same function
16	P5 ₂ /S _{CLK2} /AN ₁₀	as ports P2 ₄ - P2 ₇
17	P5₁/S _{OUT2} /AN ₉	C MOS compatible input level
18	P5 ₀ /S _{IN2} /AN ₈	C MOS 3-state output
19	P67/SRDY1/CS/SCLK12	
20	P6 _€ /S _{CLK11}	
21	P6₅/S _{out1}	
22	P6 ₄ /S _{IN1}	
23	P6₃/CNTR₁	
24	P6₂/CNTR₀	
25	P6 ₁ /PWM	
26	P6 _o	
		6-bit C MOS I/O port with the same function
27	P47/T3 _{OUT}	as ports P2, - P2,
i	P4 _e /T1 _{out}	C MOS compatible input level
28		C MOS 3-state output
29	P4 ₈ /INT ₁ /ZCR	2-bit input port
		C MOS compatible input level
	P4./INT.	6-bit C MOS I/O port with the same function
31	P4 ₃ /INT ₃	as ports P2 ₄ - P2 ₇
32	P4 ₂ /INT ₂	C MOS compatible input level
33	P4 ₁	C MOS 3-state output
34	P4dINTo	2-bit input port C MOS compatible input level
1		
35	RESET	Reset input pin for active "L"
		4-bit C MOS I/O port with the same function
36		as ports P2 ₄ - P2 ₇
37	PB₀/X _{out}	C MOS compatible input level C MOS 3-state output
		Input and output pins for the main clock
		generating circuit.
		Feedback resistor is built in between XIN
		pin and Xout pin.
		Connect a ceramic resonator or a quartz-
	l. <u>.</u>	crystal oscillator between the X _{IN} pin and
38	X _{IN}	X _{our} pin to set oscillation frequency.
39	X _{OUT}	If an external clock is used, connect the
		clock source to the X _{IN} pin and leave the X _{OUT} pin open.
		This clock is used as the oscillating source
		of system clock.
40	V _{ss}	Apply voltage of 0V to V _{ss}
41	P2 ₇	4-bit I/O port
**'	-7	I/O direction register allows each pin to
42	P2 ₆	individually programmed as either input or
"-		output.
43	P2 ₅	At reset this port is set to input mode.
44	P2 ₄	TTL input level
	· · · · · · · · · · · · · · · · · · ·	C MOS 3-state output

47 P2,/DIG₁, 48 P2,/DIG₁s 49 P1,/DIG₁s 50 P1,/DIG₁s 51 P1,/DIG₁s 52 P1,/DIG₁s 53 P1,/DIG₁s 54 P1,/DIG₁s 55 P1,/SEG₄,/DIG₂ 56 P1,/SEG₃,/DIG₂ 57 P0,/SEG₃,/DIG₃ 58 P0,/SEG₃,/DIG₃ 60 P0,/SEG₃,/DIG₃ 61 P0,/SEG₃,/DIG₃ 62 P0,/SEG₃,/DIG₃ 63 P0,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P3,/SEG₃,/DIG₃ 66 P3,/SEG₃,/DIG₃ 67 P3,/SEG₃,/DIG₃ 68 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 60 P3,/SEG₃,/DIG₃ 61 P0,/SEG₃,/DIG₃ 62 P0,/SEG₃,/DIG₃ 63 P0,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P3,/SEG₃,/DIG₃ 66 P3,/SEG₃,/DIG₃ 67 P3,/SEG₃,/DIG₃ 68 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 60 P3,/SEG₃,/DIG₃ 61 P3,/SEG₃,/DIG₃ 62 P3,/SEG₃,/DIG₃ 63 P3,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P3,/SEG₃,/DIG₃ 66 P3,/SEG₃,/DIG₃ 67 P3,/SEG₃,/DIG₃ 68 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 60 P3,/SEG₃,/DIG₃ 61 P0,/SEG₃,/DIG₃ 62 P3,/SEG₃,/DIG₃ 63 P3,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P3,/SEG₃,/DIG₃ 66 P3,/SEG₃,/DIG₃ 67 P3,/SEG₃,/DIG₃ 68 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 60 P3,/SEG₃,/DIG₃ 61 P0,/SEG₃,/DIG₃ 62 P3,/SEG₃,/DIG₃ 63 P3,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P3,/SEG₃,/DIG₃ 66 P3,/SEG₃,/DIG₃ 67 P3,/SEG₃,/DIG₃ 68 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 60 P3,/SEG₃,/DIG₃ 61 P0,/SEG₃,/DIG₃ 62 P1,/SEG₃,/DIG₃ 63 P1,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P1,/SEG₃,/DIG₃ 66 P1,/SEG₃,/DIG₃ 67 P1,/SEG₃,/DIG₃ 68 P1,/SEG₃,/DIG₃ 68 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 61 P1,/SEG₃,/DIG₃ 61 P1,/SEG₃,/DIG₃ 62 P1,/SEG₃,/DIG₃ 63 P1,/SEG₃,/DIG₃ 64 P1,/SEG₃,/DIG₃ 65 P1,/SEG₃,/DIG₃ 66 P1,/SEG₃,/DIG₃ 66 P1,/SEG₃,/DIG₃ 67 P1,/SEG₃,/DIG₃ 68 P1,/SEG₃,/DIG₃ 68 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 61 P1,/SEG₃,/DIG₃ 61 P1,/SEG₃,/DIG₃ 61 P1,/SEG₃,/DIG₃ 62 P1,/SEG₃,/DIG₃ 62 P1,/SEG₃,/DIG₃ 63 P1,/SEG₃,/DIG₃ 64 P1,/SEG₃,/DIG₃ 65 P1,/SEG₃,/DIG₃ 65 P1,/SEG₃,/DIG₃ 66 P1,/SEG₃,/DIG₃ 66 P1,/SEG₃,/DIG₃ 67 P1,/SEG₃,/DIG₃ 68 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG	Pin	Name	Function
47 P2,/DIG₁, 48 P2,/DIG₁s 49 P1,/DIG₁s 50 P1,/DIG₁s 51 P1,/DIG₁s 52 P1,/DIG₁s 53 P1,/DIG₁s 54 P1,/DIG₁s 55 P1,/SEG₄,/DIG₂ 56 P1,/SEG₃,/DIG₂ 57 P0,/SEG₃,/DIG₃ 58 P0,/SEG₃,/DIG₃ 60 P0,/SEG₃,/DIG₃ 61 P0,/SEG₃,/DIG₃ 62 P0,/SEG₃,/DIG₃ 63 P0,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P3,/SEG₃,/DIG₃ 66 P3,/SEG₃,/DIG₃ 67 P3,/SEG₃,/DIG₃ 68 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 60 P3,/SEG₃,/DIG₃ 61 P0,/SEG₃,/DIG₃ 62 P0,/SEG₃,/DIG₃ 63 P0,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P3,/SEG₃,/DIG₃ 66 P3,/SEG₃,/DIG₃ 67 P3,/SEG₃,/DIG₃ 68 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 60 P3,/SEG₃,/DIG₃ 61 P3,/SEG₃,/DIG₃ 62 P3,/SEG₃,/DIG₃ 63 P3,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P3,/SEG₃,/DIG₃ 66 P3,/SEG₃,/DIG₃ 67 P3,/SEG₃,/DIG₃ 68 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 60 P3,/SEG₃,/DIG₃ 61 P0,/SEG₃,/DIG₃ 62 P3,/SEG₃,/DIG₃ 63 P3,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P3,/SEG₃,/DIG₃ 66 P3,/SEG₃,/DIG₃ 67 P3,/SEG₃,/DIG₃ 68 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 60 P3,/SEG₃,/DIG₃ 61 P0,/SEG₃,/DIG₃ 62 P3,/SEG₃,/DIG₃ 63 P3,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P3,/SEG₃,/DIG₃ 66 P3,/SEG₃,/DIG₃ 67 P3,/SEG₃,/DIG₃ 68 P3,/SEG₃,/DIG₃ 69 P3,/SEG₃,/DIG₃ 60 P3,/SEG₃,/DIG₃ 61 P0,/SEG₃,/DIG₃ 62 P1,/SEG₃,/DIG₃ 63 P1,/SEG₃,/DIG₃ 64 P0,/SEG₃,/DIG₃ 65 P1,/SEG₃,/DIG₃ 66 P1,/SEG₃,/DIG₃ 67 P1,/SEG₃,/DIG₃ 68 P1,/SEG₃,/DIG₃ 68 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 61 P1,/SEG₃,/DIG₃ 61 P1,/SEG₃,/DIG₃ 62 P1,/SEG₃,/DIG₃ 63 P1,/SEG₃,/DIG₃ 64 P1,/SEG₃,/DIG₃ 65 P1,/SEG₃,/DIG₃ 66 P1,/SEG₃,/DIG₃ 66 P1,/SEG₃,/DIG₃ 67 P1,/SEG₃,/DIG₃ 68 P1,/SEG₃,/DIG₃ 68 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 60 P1,/SEG₃,/DIG₃ 61 P1,/SEG₃,/DIG₃ 61 P1,/SEG₃,/DIG₃ 61 P1,/SEG₃,/DIG₃ 62 P1,/SEG₃,/DIG₃ 62 P1,/SEG₃,/DIG₃ 63 P1,/SEG₃,/DIG₃ 64 P1,/SEG₃,/DIG₃ 65 P1,/SEG₃,/DIG₃ 65 P1,/SEG₃,/DIG₃ 66 P1,/SEG₃,/DIG₃ 66 P1,/SEG₃,/DIG₃ 67 P1,/SEG₃,/DIG₃ 68 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG₃ 69 P1,/SEG₃,/DIG			
48 P2_VDIG_s 49 P1_VDIG_s 50 P1_VDIG_s 51 P1_VDIG_s 52 P1_VDIG_s 53 P1_VDIG_s 54 P1_VDIG_s 55 P1_VSEG_s_VDIG_s 56 P1_VSEG_s_VDIG_s 56 P1_VSEG_s_VDIG_s 57 P0_VSEG_s_VDIG_s 68 P0_VSEG_s_VDIG_s 69 P0_VSEG_s_VDIG_s 61 P0_VSEG_s_VDIG_s 61 P0_VSEG_s_VDIG_s 62 P0_VSEG_s_VDIG_s 63 P0_VSEG_s_VDIG_s 64 P0_VSEG_s_VDIG_s 65 P3_VSEG_s_VDIG_s 66 P3_VSEG_s_VDIG_s 67 P3_VSEG_s_S 68 P3_VSEG_s_s 68 P3_VSEG_s_s 69 P3_VSEG_s_s 69 P3_VSEG_s_s 71 P3_VSEG_s_s 71 P3_VSEG_s_s 72 P3_VSEG_s_s 73 P9_VSEG_s_s 74 P9_VSEG_s_s 75 P9_VSEG_s_s 76 P9_VSEG_s_s 77 P9_VSEG_s_s 78 P9_VSEG_s_s 79 P9_VSEG_s_s 79 P9_VSEG_s_s 80 P3_VSEG_s_s 81 P8_VSEG_s_s 81 P8_VSEG_s_s 82 P8_VSEG_s_s 84 P8_VSEG_s_s 85 P8_VSEG_s_s 86 P8_VSEG_s_s 87 P8_VSEG_s_s 87 P8_VSEG_s_s 88 P8_VSEG_s_s 89 P3_VSEG_s_s 89 P3_VSEG_s_s 80 P3_VSEG_s_s 80 P3_VSEG_s_s 81 P8_VSEG_s_s 81 P8_VSEG_s_s 82 P8_VSEG_s_s 83 P8_VSEG_s_s 84 P8_VSEG_s_s 85 P8_VSEG_s_s 86 P8_VSEG_s_s 87 P8_VSEG_s_s 88 P8_VSEG_s_s 89 P3_VSEG_s_s 91 V_cc_s Apply voltage of 4.0 to 5.5V to V_cc_s 92 P3_VSEG_s_s 93 P3_VSEG_s_s 94 P3_VSEG_s_s 95 P3_VSEG_s_s 96 P3_VSEG_s_s 97 P3_VSEG_s_s 97 P3_VSEG_s_s 98 P3_VSEG_s_s 99 P3_VSEG_	46	P2₂/DIG₁₅	4-bit output port with the same function as port P0
49 P1,/DIG ₁₅ 50 P1,/DIG ₁₆ 51 P1,/DIG ₁₇ 52 P1,/DIG ₁₇ 53 P1,/DIG ₁₇ 54 P1,/DIG ₁₇ 55 P1,/SEG ₄ ,/DIG ₉ 56 P1,/SEG ₄ ,/DIG ₉ 56 P1,/SEG ₃ ,/DIG ₉ 57 P0,/SEG ₃ ,/DIG ₉ 58 P0,/SEG ₃ ,/DIG ₉ 58 P0,/SEG ₃ ,/DIG ₉ 59 P0,/SEG ₃ ,/DIG ₉ 59 P0,/SEG ₃ ,/DIG ₉ 50 P3,/SEG ₃ , P0,/SEG ₃ ,/DIG ₉ 50 P3,/SEG ₃ , P0,/SEG ₃ ,/DIG ₉ 50 P3,/SEG ₃ , P0,/SEG	47	P2 ₁ /DIG ₁₇	
50			
51 P1/JDIG ₁₃ 52 P1/JDIG ₁₀ 53 P1/JDIG ₁₀ 54 P1/JDIG ₁₀ 55 P1/JSEG ₄ /JDIG ₆ 56 P1/JSEG ₄ /DIG ₆ 56 P1/JSEG ₃ /DIG ₆ 57 P0/JSEG ₃ /DIG ₆ 58 P0/JSEG ₃ /DIG ₆ 59 P0/JSEG ₃ /DIG ₆ 61 P0/JSEG ₃ /DIG ₆ 62 P0/JSEG ₃ /DIG ₆ 63 P0/JSEG ₃ /DIG ₆ 64 P0/JSEG ₃ /DIG ₆ 65 P3/JSEG ₃ 66 P3/JSEG ₃ 66 P3/JSEG ₃ 67 P3/JSEG ₃ 68 P3/JSEG ₃ 69 P3/JSEG	49	P1 ₇ /DIG ₁₅	
S2 P1,/DIG1,2 S-bit output port with the same function as port	50	P1 ₆ /DIG ₁₄	
53 P1/JDIG11 54 P1/JDIG10 55 P1/JSEGa,/DIG0 56 P1/JSEGas/DIG0 57 P0J/SEGas/DIG0 58 P0J/SEGas/DIG0 59 P0J/SEGas/DIG1 60 P0J/SEGas/DIG3 61 P0J/SEGas/DIG3 62 P0J/SEGas/DIG3 63 P0J/SEGas/DIG3 64 P0J/SEGas/DIG3 65 P3J/SEGas/DIG3 66 P3J/SEG3/DIG3 66 P3J/SEG3 67 P3J/SEG3 68 P3J/SEG3 68 P3J/SEG3 69 P3J/SEG3 70 P3J/SEG3 71 P3J/SEG3 72 P3J/SEG3 73 P9J/SEG3 74 P9J/SEG3 75 P9J/SEG3 76 P9J/SEG3 77 P9J/SEG3 78 P9J/SEG3 79 P9J/SEG3 79 P9J/SEG3 79 P9J/SEG3 78 P9J/SEG3 79 P9	51	P1 ₅ /DIG ₁₃	
54 P1 ₃ /DIG ₁₀ 55 P1 ₄ /SEG _{at} /DIG _a 56 P1 ₀ /SEG _{at} /DIG _a 57 P0 ₂ /SEG _{3t} /DIG ₃ 58 P0 ₂ /SEG _{3t} /DIG ₃ 59 P0 ₂ /SEG _{3t} /DIG ₃ 60 P0 ₂ /SEG _{3t} /DIG ₃ 61 P0 ₂ /SEG _{3t} /DIG ₃ 62 P0 ₂ /SEG _{3t} /DIG ₃ 63 P0 ₂ /SEG _{3t} /DIG ₃ 64 P0 ₂ /SEG _{3t} /DIG ₃ 65 P3 ₃ /SEG _{3t} 66 P3 ₄ /SEG _{3t} 66 P3 ₄ /SEG _{3t} 67 P3 ₃ /SEG _{3t} 68 P3 ₄ /SEG _{3t} 69 P3 ₃ /SEG _{3t} 69 P3 ₃ /SEG _{3t} 69 P3 ₄ /SEG _{3t} 60 P3 ₄ /SEG _{3t} 60 P3 ₄ /SEG _{3t} 60 P3 ₄ /SEG _{3t} 61 P3 ₄ /SEG _{3t} 62 P3 ₄ /SEG _{3t} 63 P3 ₄ /SEG _{3t} 64 P5 ₄ /SEG _{3t} 65 P3 ₄ /SEG _{3t} 66 P3 ₄ /SEG _{3t} 67 P3 ₄ /SEG _{3t} 68 P3 ₄ /SEG _{3t} 69 P3 ₄ /SEG _{3t} 60 P	52	P1 ₄ /DIG ₁₂	8-bit output port with the same function as port P0
55 P1,/SEGa,/DIGa 56 P1,/SEGas/DIGa 57 P0,/SEGas/DIGa 58 P0,/SEGas/DIGa 59 P0,/SEGas/DIGa 60 P0,/SEGas/DIGa 61 P0,/SEGas/DIGa 62 P0,/SEGas/DIGa 63 P0,/SEGas/DIGa 64 P0,/SEGas/DIGa 65 P3,/SEGas/DIGa 66 P3,/SEGas/DIGa 66 P3,/SEGas 67 P3,/SEGas 68 P3,/SEGas 69 P3,/SEGas 70 P3,/SEGas 71 P3,/SEGas 71 P3,/SEGas 72 P3,/SEGas 73 P9,/SEGas 74 P9,/SEGas 75 P9,/SEGas 76 P9,/SEGas 77 P9,/SEGas 78 P9,/SEGas 79 P8,/SEGas 80 P8,/SEGas 81 P8,/SEGas 82 P8,/SEGas 83 P8,/SEGas 84 P8,/SEGas 85 P8,/SEGas 86 P8,/SEGas 87 P8,/SEGas 87 P8,/SEGas 88 P8,/SEGas 89 PA,/SEGas 89 PA,/SEGas 89 PA,/SEGas 89 PA,/SEGas 89 PA,/SEGas 89 PA,/SEGas 80 PA,/SEGas 80 PA,/SEGas 81 P8,/SEGas 82 PA,/SEGas 83 PA,/SEGas 84 P8,/SEGas 85 PA,/SEGas 86 PA,/SEGas 87 PA,/SEGas 88 PA,/SEGas 89 PA,/SEGas 90 PA,/SEGas 91 Voc Apply voltage of 4.0 to 5.5V to Voc 92 PA,/SEGa 94 PA,/SEGa 95 PA,/SEGa 96 PA,/SEGa 97 PA,/SEGa 97 PA,/SEGa 98 PA,/SEGa 99 PA,/SEGa 99 PA,/SEGa 90 PA,/SEGa 91 PA,/SEGa 91 PA,/SEGa 92 PA,/SEGa 93 PA,/SEGa 94 PA,/SEGa 95 PA,/SEGa 96 PA,/SEGa 97 PA,/SEGa 97 PA,/SEGa 98 PA,/SEGa 99 PA,/SEGa 99 PA,/SEGa 90 PA,/SEGa 91 PA,/SEGa 91 PA,/SEGa 92 PA,/SEGa 93 PA,/SEGa 94 PA,/SEGa 95 PA,/SEGa 96 PA,/SEGa 97 PA,/SEGa 98 PA,/SEGa 99 PA,/SEGa 99 PA,/SEGa 99 PA,/SEGa 90 PA,/SEGa 91 PA,/SEGa 91 PA,/SEGa 92 PA,/SEGa 93 PA,/SEGa 94 PA,/SEGa 95 PA,/SEGa 96 PA,/SEGa 97 PA,/SEGa 98 PA,/SEGa 99 PA,/SEGa	53	P1 ₃ /DIG ₁₁	
56 P1₀/SEG₃₀/DIG₃ 57 P0₀/SEG₃₀/DIG₃ 58 P0₀/SEG₃₀/DIG₃ 59 P0₀/SEG₃₀/DIG₃ 60 P0₀/SEG₃₀/DIG₃ 61 P0₀/SEG₃₀/DIG₃ 62 P0₀/SEG₃₀/DIG₃ 63 P0₀/SEG₃₀/DIG₃ 64 P0₀/SEG₃₀/DIG₃ 64 P0₀/SEG₃₀/DIG₃ 65 P3₀/SEG₃₃ 66 P3₀/SEG₃₃ 66 P3₀/SEG₃₃ 67 P3₀/SEG₃₃ 68 P3₀/SEG₃₃ 69 P3₀/SEG₃₃ 69 P3₀/SEG₃₃ 69 P3₀/SEG₃₃ 69 P3₀/SEG₃₃ 70 P3₀/SEG₃₃ 70 P3₀/SEG₃₃ 71 P3₀/SEG₃₃ 72 P3₀/SEG₃₃ 73 P9₀/SEG₃₃ 74 P9₀/SEG₃₂ 75 P9₀/SEG₃₃ 76 P9₀/SEG₃ 77 P9₀/SEG₃ 78 P9₀/SEG₃ 79 P9₀/SEG₃ 80 P8₀/SEG₃ 80 P8₀/SEG₃ 81 P8₀/SEG₃ 82 P8₀/SEG₃ 83 P8₀/SEG₃ 84 P8₀/SEG₃ 85 P8₀/SEG₃ 86 P8₀/SEG₃ 87 P8₀/SEG₃ 87 P8₀/SEG₃ 88 P8₀/SEG₃ 98 P8₀/SEG₃ 99 Pa₀/SEG₃ 90 Pa₀/SEG₃ 91 Vcc 92 Pa₀/SEG₃ 94 Pa₀/SEG₃ 95 Pa₀/SEG₃ 96 Pa⟩/SEG₃ 97 Pa₀/SEG₃ 98 Pa₀/SEG₃ 99 Pa₀/SEG₃ 90 Pa₀/SEG₃ 91 Vcc 92 Pa₀/SEG₃ 93 Pa₀/SEG₃ 94 Pa₀/SEG₃ 95 Pa₀/SEG₃ 96 Pa⟩/SEG₃ 97 Pa₀/SEG₃ 98 Pa⟩/SEG₃ 99 Pa₀/SEG₃ 90 Pa₀/SEG₃ 90 Pa₀/SEG₃ 91 Vcc 92 Pa₀/SEG₃ 94 Pa₀/SEG₃ 95 Pa₀/SEG₃ 96 Pa⟩/SEG₃ 97 Pa₀/SEG₃ 98 Pa⟩/SEG₃ 99 Pa₀/SEG₃ 99 Pa⟩/SEG₃ 90 Pa⟩/SEG₃ 90 Pa⟩/SEG₃ 90 Pa⟩/SEG₃ 91 Vcc 92 Pa₀/SEG₃ 93 Pa₀/SEG₃ 94 Pa₀/SEG₃ 95 Pa⟩/SEG₃ 96 Pa⟩/SEG₃ 97 Pa⟩/SEG₃ 98 Pa⟩/SEG₃ 99 Pa⟩/SEG₃ 99 Pa⟩/SEG₃ 90 Pa	54	P1₂/DIG₁₀	
57 P0 ₃ /SEG _{3a} /DIG ₅ 58 P0 _b /SEG _{3a} /DIG ₆ 59 P0 _b /SEG _{3a} /DIG ₆ 60 P0 _b /SEG _{3a} /DIG ₆ 61 P0 ₃ /SEG _{3a} /DIG ₃ 62 P0 _b /SEG _{3a} /DIG ₃ 63 P0 _b /SEG _{3a} /DIG ₂ 63 P0 _b /SEG _{3a} /DIG ₂ 64 P0 _b /SEG _{3a} /DIG ₂ 65 P3 _b /SEG _{3a} /DIG ₃ 66 P3 _b /SEG _{3a} /DIG ₃ 67 P3 _b /SEG _{3a} /DIG ₃ 68 P3 _b /SEG _{3a} /DIG ₃ 69 P3 _b /SEG _{3a} 69 P3 _b /SEG _{2a} 70 P3 _b /SEG _{2a} 71 P3 _b /SEG _{2a} 72 P3 _b /SEG _{2a} 73 P9 _b /SEG _{2a} 74 P9 _b /SEG _{2a} 75 P9 _b /SEG _{2a} 76 P9 _b /SEG _{2a} 77 P9 _b /SEG _{2a} 78 P9 _b /SEG _{1a} 79 P9 _b /SEG _{1a} 80 P9 _b /SEG _{1a} 81 P8 _b /SEG _{1a} 82 P8 _b /SEG _{1a} 83 P8 _b /SEG _{1a} 84 P8 _b /SEG _{1a} 85 P8 _b /SEG _{1a} 86 P8 _b /SEG _{1a} 87 P8 _b /SEG _{1a} 88 P8 _b /SEG _{1a} 89 PA _b /SEG _a 81 P8 _b /SEG _a 89 PA _b /SEG _a 80 PA _b /SEG _a 81 P8 _b /SEG _a 81 P8 _b /SEG _a 82 P8 _b /SEG _a 83 P8 _b /SEG _a 84 P8 _b /SEG _a 85 P8 _b /SEG _a 86 P8 _b /SEG _a 87 P8 _b /SEG _a 88 P8 _b /SEG _a 89 PA _b /SEG _a 80 PA _b /SEG _a 80 PA _b /SEG _a 81 P8 _b /SEG _a 82 P8 _b /SEG _a 83 PA _b /SEG _a 84 P8 _b /SEG _a 85 P8 _b /SEG _a 86 P8 _b /SEG _a 87 P8 _b /SEG _a 88 P8 _b /SEG _a 89 PA _b /SEG _a 80 PA _b /SEG _a 80 PA _b /SEG _a 81 P8 _b /SEG _a 82 P8 _b /SEG _a 83 PA _b /SEG _a 84 P8 _b /SEG _a 85 Pa _b /SEG _a 86 PA _b /SEG _a 86 PA _b /SEG _a 87 Pa _b /SEG _a 88 P8 _b /SEG _a 89 PA _b /SEG _a 90 PA _b /SEG _a 91 V _c 92 PA _b /SEG _a 93 PA _b /SEG _a 94 PA _b /SEG _a 95 PA _b /SEG _a 96 PA _b /SEG _a 97 PA _b /SEG _a 98 PA _b /SEG _a 99 PA _b /SEG	55	P1,/SEG4,/DIG	
8-bit output port This port builds in pull-down resistor between port P0 _x /SEG _{3x} /DIG ₃ 60 P0 _x /SEG _{3x} /DIG ₃ 61 P0 _y /SEG _{3x} /DIG ₃ 62 P0 _z /SEG _{3x} /DIG ₃ 63 P0 _y /SEG _{3x} /DIG ₃ 64 P0 _y /SEG _{3x} /DIG ₃ 65 P3 _x /SEG _{3x} 66 P3 _x /SEG _{3x} 67 P3 _y /SEG _{3x} 68 P3 _x /SEG _{2x} 69 P3 _x /SEG _{2x} 70 P3 _z /SEG _{2x} 71 P3 _y /SEG _{2x} 72 P3 _y /SEG _{2x} 73 P9 _y /SEG _{2x} 74 P9 _y /SEG _{2x} 75 P9 _y /SEG _{2x} 76 P9 _x /SEG _x 77 P9 _y /SEG _x 78 P9 _x /SEG _x 8-bit output port with the same function as port port port port with the same function as port port port port with the same function as port port port port port port port port	56	P1 ₆ /SEG ₄₆ /DIG ₈	
59 P0 ₈ /SEG _{3s} /DIG ₈ 60 P0 ₈ /SEG _{3s} /DIG ₉ 61 P0 ₉ /SEG _{3s} /DIG ₂ 62 P0 ₉ /SEG _{3s} /DIG ₂ 63 P0 ₁ /SEG _{3s} /DIG ₂ 64 P0 ₉ /SEG _{3s} /DIG ₁ 64 P0 ₉ /SEG _{3s} /DIG ₁ 65 P3 ₇ /SEG _{3s} 66 P3 ₈ /SEG _{3s} 67 P3 ₈ /SEG _{2s} 68 P3 ₈ /SEG _{2s} 69 P3 ₈ /SEG _{2s} 70 P3 ₂ /SEG _{2s} 71 P3 ₁ /SEG _{2s} 72 P3 ₉ /SEG _{2s} 73 P9 ₉ /SEG _{2s} 74 P9 ₉ /SEG _{2s} 75 P9 ₈ /SEG _{2s} 76 P9 ₈ /SEG _{2s} 77 P9 ₉ /SEG _{1s} 78 P9 ₉ /SEG _{1s} 79 P9 ₉ /SEG _{1s} 89 P3 ₈ /SEG _{1s} 81 P3 ₈ /SEG _{1s} 82 P3 ₈ /SEG _{1s} 84 P3 ₈ /SEG _{1s} 85 P3 ₈ /SEG _{1s} 86 P3 ₈ /SEG _{1s} 87 P3 ₈ /SEG _{1s} 88 P3 ₈ /SEG _{1s} 89 P3 ₈ /SEG _{1s} 80 P3 ₈ /SEG _{1s} 81 P3 ₈ /SEG _{1s} 82 P3 ₈ /SEG _{1s} 83 P3 ₈ /SEG _{1s} 84 P3 ₈ /SEG _{1s} 85 P3 ₈ /SEG _{1s} 86 P3 ₈ /SEG _{1s} 87 P3 ₈ /SEG _{1s} 88 P3 ₈ /SEG _{1s} 89 P3 ₈ /SEG _{1s} 89 P3 ₈ /SEG ₃ 89 P	57	P07/SEG39/DIG7	
60 P0_/SEG_3/DIG_4 61 P0_/SEG_3/DIG_3 62 P0_/SEG_3/DIG_3 63 P0_/SEG_3/DIG_0 64 P0_/SEG_3/DIG_0 65 P3_/SEG_3 66 P3_/SEG_3 67 P3_/SEG_3 68 P3_/SEG_3 69 P3_/SEG_3 69 P3_/SEG_3 69 P3_/SEG_3 69 P3_/SEG_3 69 P3_/SEG_3 70 P3_/SEG_3 71 P3_/SEG_3 72 P3_/SEG_3 73 P9_/SEG_3 74 P9_/SEG_3 75 P9_/SEG_3 76 P9_/SEG_3 77 P9_/SEG_1 78 P9_/SEG_1 79 P8_/SEG_1 79 P8_/SEG_1 79 P8_/SEG_1 79 P8_/SEG_1 79 P8_/SEG_2 79 P8_/SEG_3 7	58	P0 ₆ /SEG ₃₆ /DIG ₆	8-bit output port
61 P0_/SEG_3/DIG_3 62 P0_/SEG_3/DIG_2 63 P0_/SEG_3/DIG_0 64 P0_/SEG_3/DIG_0 65 P3_/SEG_3 66 P3_/SEG_3 67 P3_/SEG_3 68 P3_/SEG_3 69 P3_/SEG_2 69 P3_/SEG_2 70 P3_/SEG_2 71 P3_/SEG_2 71 P3_/SEG_2 72 P3_/SEG_2 73 P9_/SEG_2 74 P9_/SEG_2 75 P9_/SEG_2 76 P9_/SEG_3 77 P9_/SEG_3 78 P9_/SEG_3 79 P9_/SEG_3 79 P9_/SEG_3 79 P9_/SEG_3 79 P9_/SEG_3 79 P9_/SEG_3 79 P9_/SEG_1 79 P8_/SEG_1 79 P8_/SEG_1 79 P8_/SEG_2 79 P8_/SEG_2 79 P8_/SEG_2 79 P8_/SEG_2 79 P8_/SEG_2 79 P8_/SEG_2 79 P8_/SEG_3 79 P8_/	59	P0 ₅ /SEG ₃₇ /DIG ₅	This port builds in pull-down resistor between
62 P0s/SEGsa/DIGs 63 P0s/SEGsa/DIGs 64 P0s/SEGsa/DIGs 65 P3s/SEGs 66 P3s/SEGs 67 P3s/SEGs 68 P3s/SEGs 69 P3s/SEGs 69 P3s/SEGs 69 P3s/SEGs 69 P3s/SEGs 70 P3s/SEGs 71 P3s/SEGs 72 P3s/SEGs 73 P9s/SEGs 74 P9s/SEGs 75 P9s/SEGs 76 P9s/SEGs 77 P9s/SEGs 78 P9s/SEGs 79 P9s/SEGs 80 P9s/SEGs 80 P9s/SEGs 80 P9s/SEGs 80 P9s/SEGs 80 P9s/SEGs 80 P8s/SEGs	60	P04/SEG36/DIG4	port P0 and the V _{EE} pin
63 P0,/SEG ₃₂ /DIG ₁ 64 P0 ₆ /SEG ₃₂ /DIG ₀ 65 P3 ₇ /SEG ₃₁ 66 P3 ₈ /SEG ₃₀ 67 P3 ₈ /SEG ₂₀ 68 P3 ₈ /SEG ₂₂ 70 P3 ₈ /SEG ₂₂ 71 P3,/SEG ₂₂ 72 P3 ₈ /SEG ₂₂ 73 P9 ₈ /SEG ₂₂ 74 P9 ₈ /SEG ₂₂ 75 P9 ₈ /SEG ₂₂ 76 P9 ₈ /SEG ₂₂ 77 P9 ₈ /SEG ₂₀ 78 P9 ₈ /SEG ₁₀ 79 P9 ₈ /SEG ₁₀ 79 P9 ₈ /SEG ₁₀ 80 P9 ₈ /SEG ₁₁ 81 P8 ₈ /SEG ₁₂ 82 P8 ₈ /SEG ₁₂ 83 P8 ₈ /SEG ₁₂ 84 P8 ₈ /SEG ₁₂ 85 P8 ₈ /SEG ₁₃ 86 P8 ₈ /SEG ₁₂ 87 P8 ₈ /SEG ₁₃ 88 P8 ₈ /SEG ₁₄ 89 P8 ₈ /SEG ₁₅ 80 P8 ₈ /SEG ₁₆ 81 P8 ₈ /SEG ₁₆ 82 P8 ₈ /SEG ₁₆ 83 P8 ₈ /SEG ₁₆ 84 P8 ₈ /SEG ₁₆ 85 P8 ₈ /SEG ₁₆ 86 P8 ₈ /SEG ₁₆ 87 P8 ₈ /SEG ₁₆ 88 P8 ₈ /SEG ₁₆ 89 PA ₈ /SEG ₆ 89 PA ₈ /SEG ₆ 89 PA ₈ /SEG ₆ 91 V _{CC} Apply voltage of 4.0 to 5.5V to V _{CC} 92 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 92 PA ₈ /SEG ₆ 93 PA ₈ /SEG ₆ 94 PA ₈ /SEG ₆ 95 PA ₈ /SEG ₆ 96 PA ₈ /SEG ₆ 97 PA ₈ /SEG ₆ 98 PA ₈ /SEG ₆ 99 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 92 PA ₈ /SEG ₆ 93 PA ₈ /SEG ₆ 94 PA ₈ /SEG ₆ 95 PA ₈ /SEG ₆ 96 PA ₈ /SEG ₆ 97 PA ₈ /SEG ₆ 98 PA ₈ /SEG ₆ 99 PA ₈ /SEG ₆ 99 PA ₈ /SEG ₆ 90 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 92 PA ₈ /SEG ₆ 93 PA ₈ /SEG ₆ 94 PA ₈ /SEG ₆ 95 PA ₈ /SEG ₆ 96 PA ₈ /SEG ₆ 97 PA ₈ /SEG ₆ 98 PA ₈ /SEG ₆ 99 PA ₈ /SEG ₆ 99 PA ₈ /SEG ₆ 99 PA ₈ /SEG ₆ 90 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 92 PA ₈ /SEG ₆ 93 PA ₈ /SEG ₆ 94 PA ₈ /SEG ₆ 95 PA ₈ /SEG ₆ 96 PA ₈ /SEG ₆ 97 PA ₈ /SEG ₆ 98 PA ₈ /SEG ₆ 99 PA ₈ /SEG ₆ 99 PA ₈ /SEG ₆ 99 PA ₈ /SEG ₆ 90 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 92 PA ₈ /SEG ₆ 93 PA ₈ /SEG ₆ 94 PA ₈ /SEG ₆ 95 PA ₈ /SEG ₆ 96 PA ₈ /SEG ₆ 97 PA ₈ /SEG ₆ 98 PA ₈ /SEG ₆ 99 PA ₈ /SEG ₆ 90 PA ₈ /SEG ₆ 90 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 91 PA ₈ /SEG ₆ 92 PA ₈ /SEG ₆ 93 PA ₈ /SEG ₆ 94 PA ₈ /SEG ₆ 95 PA ₈ /SEG ₆ 96 PA ₈ /SEG ₆ 97 PA ₈ /SEG ₆	61	P0 ₃ /SEG ₃₅ /DIG ₃	At reset this port is set to VEE level
64 P0 ₀ /SEG ₃₂ /DIG ₀ 65 P3 ₇ /SEG ₃₁ 66 P3 ₆ /SEG ₃₀ 67 P3 ₆ /SEG ₂₀ 68 P3 ₆ /SEG ₂₀ 68 P3 ₇ /SEG ₂₀ 68 P3 ₇ /SEG ₂₀ 70 P3 ₇ /SEG ₂₀ 71 P3 ₇ /SEG ₂₀ 72 P3 ₆ /SEG ₂₀ 73 P9 ₇ /SEG ₂₀ 74 P9 ₆ /SEG ₂₂ 75 P9 ₆ /SEG ₂₂ 76 P9 ₆ /SEG ₂₁ 77 P9 ₇ /SEG ₁₀ 78 P9 ₇ /SEG ₁₀ 79 P9 ₇ /SEG ₁₀ 81 P8 ₇ /SEG ₁₅ 82 P8 ₆ /SEG ₁₃ 84 P8 ₆ /SEG ₁₃ 85 P8 ₆ /SEG ₁₃ 86 P8 ₆ /SEG ₁₃ 87 P8 ₆ /SEG ₁₃ 88 P8 ₆ /SEG ₁₃ 89 P8 ₇ /SEG ₁₀ 81 P8 ₇ /SEG ₁₀ 82 P8 ₇ /SEG ₁₀ 83 P8 ₇ /SEG ₁₀ 84 P8 ₆ /SEG ₁₀ 85 P8 ₇ /SEG ₁₀ 86 P8 ₇ /SEG ₃ 87 P8 ₇ /SEG ₃ 88 P8 ₇ /SEG ₃ 89 P3 ₇ /	62	P0 ₂ /SEG ₃₄ /DIG ₂	The high-breakdown-voltage P-channel open-
64 P0 ₀ /SEG ₃₂ /DIG ₀ 65 P3 ₇ /SEG ₃₁ 66 P3 ₆ /SEG ₃₀ 67 P3 ₆ /SEG ₂₀ 68 P3 ₆ /SEG ₂₀ 68 P3 ₇ /SEG ₂₀ 68 P3 ₇ /SEG ₂₀ 70 P3 ₇ /SEG ₂₀ 71 P3 ₇ /SEG ₂₀ 72 P3 ₆ /SEG ₂₀ 73 P9 ₇ /SEG ₂₀ 74 P9 ₆ /SEG ₂₂ 75 P9 ₆ /SEG ₂₂ 76 P9 ₆ /SEG ₂₁ 77 P9 ₇ /SEG ₁₀ 78 P9 ₇ /SEG ₁₀ 79 P9 ₇ /SEG ₁₀ 81 P8 ₇ /SEG ₁₅ 82 P8 ₆ /SEG ₁₃ 84 P8 ₆ /SEG ₁₃ 85 P8 ₆ /SEG ₁₃ 86 P8 ₆ /SEG ₁₃ 87 P8 ₆ /SEG ₁₃ 88 P8 ₆ /SEG ₁₃ 89 P8 ₇ /SEG ₁₀ 81 P8 ₇ /SEG ₁₀ 82 P8 ₇ /SEG ₁₀ 83 P8 ₇ /SEG ₁₀ 84 P8 ₆ /SEG ₁₀ 85 P8 ₇ /SEG ₁₀ 86 P8 ₇ /SEG ₃ 87 P8 ₇ /SEG ₃ 88 P8 ₇ /SEG ₃ 89 P3 ₇ /	63	P0,/SEG ₃₃ /DIG,	drain
65 P3/SEG ₃₁ 66 P3 ₆ /SEG ₃₀ 67 P3 ₆ /SEG ₃₀ 68 P3 ₆ /SEG ₂₆ 8 P3 ₆ /SEG ₂₇ 70 P3 ₆ /SEG ₂₆ 71 P3 ₇ /SEG ₂₆ 72 P3 ₆ /SEG ₂₆ 73 P9 ₇ /SEG ₂₂ 74 P9 ₆ /SEG ₂₂ 75 P9 ₆ /SEG ₂₂ 76 P9 ₆ /SEG ₂₀ 77 P9 ₃ /SEG ₃₀ 78 P9 ₇ /SEG ₃₀ 78 P9 ₇ /SEG ₁₀ 79 P9 ₇ /SEG ₁₀ 79 P9 ₇ /SEG ₁₀ 8-bit output port with the same function as port p9 ₇ /SEG ₁₀ 79 P9 ₇ /SEG ₁₀ 8-bit output port with the same function as port p9 ₇ /SEG ₁₀ 8-bit output port with the same function as port p9 ₇ /SEG ₁₀ 8-bit l/O port with the same function as ports p8 ₇ /SEG ₁₁ 8-bit l/O port with the same function as ports p8 ₇ /SEG ₁₀ 87 P8 ₇ /SEG ₁₀ 88 P8 ₇ /SEG ₁₀ 89 PA ₇ /SEG ₃ 80 PA ₇			
66 P3 ₆ /SEG ₃₀ 67 P3 ₆ /SEG ₂₀ 68 P3 ₆ /SEG ₂₀ 69 P3 ₆ /SEG ₂₀ 70 P3 ₆ /SEG ₂₀ 71 P3 ₇ /SEG ₂₅ 72 P3 ₆ /SEG ₂₅ 73 P9 ₇ /SEG ₂₅ 74 P9 ₆ /SEG ₂₂ 75 P9 ₆ /SEG ₂₂ 76 P9 ₆ /SEG ₂₀ 77 P9 ₇ /SEG ₂₃ 78 P9 ₇ /SEG ₂₃ 79 P9 ₇ /SEG ₂₃ 79 P9 ₇ /SEG ₂₃ 79 P9 ₇ /SEG ₂₀ 8-bit output port with the same function as port port port port with the same function as port port port port port port port port	-		
68 P3 ₄ /SEG ₂₆ 69 P3 ₅ /SEG ₂₇ 70 P3 ₂ /SEG ₂₆ 71 P3 ₁ /SEG ₂₆ 71 P3 ₁ /SEG ₂₆ 72 P3 ₆ /SEG ₂₆ 73 P9 ₆ /SEG ₂₃ 74 P9 ₆ /SEG ₂₃ 75 P9 ₆ /SEG ₂₁ 76 P9 ₄ /SEG ₂₀ 77 P9 ₂ /SEG ₁₆ 78 P9 ₂ /SEG ₁₆ 79 P9 ₇ /SEG ₁₆ 79 P9 ₇ /SEG ₁₆ 81 P8 ₇ /SEG ₁₅ 82 P8 ₆ /SEG ₁₃ 83 P8 ₆ /SEG ₁₃ 84 P8 ₇ /SEG ₁₅ 85 P8 ₃ /SEG ₁₁ 86 P8 ₂ /SEG ₁₆ 87 P8 ₃ /SEG ₁₇ 86 P8 ₃ /SEG ₁₆ 87 P8 ₇ /SEG ₁₆ 88 P8 ₇ /SEG ₁₆ 89 PA ₇ /SEG ₆ 89 PA ₇ /SEG ₆ 89 PA ₇ /SEG ₆ 91 V _{CC} 92 PA ₈ /SEG ₆ 91 V _{CC} Apply voltage of 4.0 to 5.5V to V _{CC} 92 PA ₈ /SEG ₃ PA ₂ /SEG ₃ PA ₂ /SEG ₃ PA ₃ /SEG ₃ PA ₄ /SEG ₃ PA ₄ /SEG ₃ PA ₅ /SEG ₃ PA ₆ /SEG ₃ PA ₆ /SEG ₃ PA ₇ /S	66	P3 ₈ /SEG ₃₀	
68 P3 ₄ /SEG ₂₆ 69 P3 ₅ /SEG ₂₇ 70 P3 ₂ /SEG ₂₆ 71 P3 ₁ /SEG ₂₆ 71 P3 ₁ /SEG ₂₆ 72 P3 ₆ /SEG ₂₆ 73 P9 ₆ /SEG ₂₃ 74 P9 ₆ /SEG ₂₃ 75 P9 ₆ /SEG ₂₁ 76 P9 ₄ /SEG ₂₀ 77 P9 ₂ /SEG ₁₆ 78 P9 ₂ /SEG ₁₆ 79 P9 ₇ /SEG ₁₆ 79 P9 ₇ /SEG ₁₆ 81 P8 ₇ /SEG ₁₅ 82 P8 ₆ /SEG ₁₃ 83 P8 ₆ /SEG ₁₃ 84 P8 ₇ /SEG ₁₅ 85 P8 ₃ /SEG ₁₁ 86 P8 ₂ /SEG ₁₆ 87 P8 ₃ /SEG ₁₇ 86 P8 ₃ /SEG ₁₆ 87 P8 ₇ /SEG ₁₆ 88 P8 ₇ /SEG ₁₆ 89 PA ₇ /SEG ₆ 89 PA ₇ /SEG ₆ 89 PA ₇ /SEG ₆ 91 V _{CC} 92 PA ₈ /SEG ₆ 91 V _{CC} Apply voltage of 4.0 to 5.5V to V _{CC} 92 PA ₈ /SEG ₃ PA ₂ /SEG ₃ PA ₂ /SEG ₃ PA ₃ /SEG ₃ PA ₄ /SEG ₃ PA ₄ /SEG ₃ PA ₅ /SEG ₃ PA ₆ /SEG ₃ PA ₆ /SEG ₃ PA ₇ /S			
69 P3s/SEG2r 70 P3z/SEG2s 71 P3s/SEG2s 72 P3s/SEG2s 73 P9s/SEG2s 74 P9s/SEG2 75 P9s/SEG21 76 P9s/SEG20 77 P9s/SEG10 78 P9z/SEG10 78 P9z/SEG16 79 P9s/SEG16 81 P8s/SEG15 82 P8s/SEG16 83 P8s/SEG11 84 P8s/SEG11 85 P8s/SEG11 86 P8z/SEG10 87 P8s/SEG10 87 P8s/SEG10 88 P8s/SEG10 89 P8s/SEG10 87 P8s/SEG10 88 P8s/SEG10 89 P8s/SEG3 80 P8s/SEG3 81 P8s/SEG3 81 P8s/SEG3 82 P8s/SEG3 83 P8s/SEG10 84 P8s/SEG10 85 P8s/SEG10 86 P8z/SEG3 87 P8s/SEG3 88 P8s/SEG3 89 PAs/SEG6 89 PAs/SEG6 89 PAs/SEG6 90 PAs/SEG6 91 Vcc Apply voltage of 4.0 to 5.5V to Vcc 92 PAs/SEG3 94 PAs/SEG3 95 PAz/SEG3 95 PAz/SEG3 96 PAs/SEG3 97 PAs/SEG3 97 PAs/SEG3 98 PAz/SEG3 99 PAz/SE			8-bit output port with the same function as port P0
70 P3 ₂ /SEG ₂₈ 71 P3 ₃ /SEG ₂₈ 72 P3 ₃ /SEG ₂₄ 73 P9 ₅ /SEG ₂₃ 74 P9 ₆ /SEG ₂₂ 75 P9 ₆ /SEG ₂₂ 76 P9 ₄ /SEG ₂₀ 8-bit output port with the same function as port 77 P9 ₃ /SEG ₁₀ 78 P9 ₂ /SEG ₁₀ 79 P9 ₁ /SEG ₁₇ 80 P9 ₆ /SEG ₁₈ 79 P9 ₇ /SEG ₁₈ 81 P8 ₇ /SEG ₁₉ 82 P8 ₆ /SEG ₁₄ 83 P8 ₆ /SEG ₁₄ 83 P8 ₆ /SEG ₁₄ 84 P8 ₆ /SEG ₁₄ 85 P8 ₇ /SEG ₁₅ 86 P8 ₇ /SEG ₁₆ 87 P8 ₇ /SEG ₁₀ 88 P8 ₇ /SEG ₁₀ 89 P8 ₇ /SEG ₉ 81 P8 ₇ /SEG ₉ 82 P8 ₇ /SEG ₉ 83 PA ₇ /SEG ₉ 84 PA ₇ /SEG ₉ 85 P8 ₇ /SEG ₉ 86 PA ₇ /SEG ₉ 87 PA ₇ /SEG ₈ 88 PA ₇ /SEG ₉ 89 PA ₇ /SEG ₉ 90 PA ₈ /SEG ₆ 91 V _{CC} PA ₇ /SEG ₉ PA ₇			,
71 P3 ₁ /SEG ₂₈ 72 P3 ₀ /SEG ₂₄ 73 P9 ₇ /SEG ₂₃ 74 P9 ₆ /SEG ₂₂ 75 P9 ₆ /SEG ₂₂ 75 P9 ₆ /SEG ₂₀ 76 P9 ₇ /SEG ₂₀ 77 P9 ₃ /SEG ₁₀ 78 P9 ₇ /SEG ₁₀ 78 P9 ₇ /SEG ₁₀ 80 P9 ₇ /SEG ₁₀ 81 P8 ₇ /SEG ₁₀ 82 P8 ₆ /SEG ₁₄ 83 P8 ₆ /SEG ₁₄ 84 P8 ₆ /SEG ₁₄ 85 P8 ₆ /SEG ₁₄ 86 P8 ₇ /SEG ₁₅ 86 P8 ₇ /SEG ₁₀ 87 P8 ₇ /SEG ₁₀ 88 P8 ₇ /SEG ₁₀ 89 P8 ₇ /SEG ₁₀ 80 P8 ₇ /SEG ₁₀ 81 P8 ₇ /SEG ₁₀ 82 P8 ₇ /SEG ₁₀ 83 P8 ₇ /SEG ₁₀ 84 P8 ₇ /SEG ₁₀ 85 P8 ₇ /SEG ₁₀ 86 P8 ₇ /SEG ₁₀ 87 P8 ₇ /SEG ₂ 88 PA ₇ /SEG ₃ 89 PA ₇ /SEG ₃ 91 V _{cc} Apply voltage of 4.0 to 5.5V to V _{cc} 92 PA ₈ /SEG ₃ 95 PA ₇ /SEG ₃ 96 PA ₇ /SEG ₃ 97 PA ₇ /SEG ₃ 98 PA ₇ /SEG ₃ 99 PA ₇ /SEG ₃ 99 PA ₇ /SEG ₃ 91 PA ₇ /SEG ₃ 91 PA ₇ /SEG ₃ 92 PA ₇ /SEG ₃ 93 PA ₇ /SEG ₃ 94 PA ₇ /SEG ₃ 95 PA ₇ /SEG ₃ 96 PA ₇ /SEG ₃ 97 PA ₇ /SEG ₃ 98 PA ₇ /SEG ₃ 99)		
72 P3 _o /SEG ₂₄ 73 P9 _r /SEG ₂₃ 74 P9 _o /SEG ₂₂ 75 P9 _o /SEG ₂₂ 76 P9 _o /SEG ₂₀ 8-bit output port with the same function as port p9 _o /SEG ₁₀ 78 P9 _o /SEG ₁₀ 79 P9 _o /SEG ₁₀ 80 P9 _o /SEG ₁₀ 81 P8 _r /SEG ₁₀ 82 P8 _o /SEG ₁₃ 83 P8 _o /SEG ₁₃ 84 P8 _o /SEG ₁₃ 85 P8 _o /SEG ₁₃ 86 P8 _o /SEG ₁₃ 87 P8 _o /SEG ₁₃ 88 P8 _o /SEG ₁₃ 89 P8 _o /SEG ₁₃ 80 P9 _o /SEG ₁₃ 80 P9 _o /SEG ₁₃ 81 P8 _o /SEG ₁₃ 82 P8 _o /SEG ₁₃ 83 P8 _o /SEG ₁₃ 84 P8 _o /SEG ₁₃ 85 P8 _o /SEG ₁₃ 86 P8 _o /SEG ₁₃ 87 P8 _o /SEG ₁₃ 88 P8 _o /SEG ₁₃ 89 PA _o /SEG ₃ 80 PA _o /SEG ₃ 81 P8 _o /SEG ₃ 82 P8 _o /SEG ₃ 83 P8 _o /SEG ₃ 84 P8 _o /SEG ₃ 85 P8 _o /SEG ₃ 86 P8 _o /SEG ₃ 87 PA _o /SEG ₃ 88 P8 _o /SEG ₃ 89 PA _o /SEG ₃ 91 V _{cc} Apply voltage of 4.0 to 5.5V to V _{cc} 92 PA _o /SEG ₃ 93 PA _o /SEG ₃ 94 PA _o /SEG ₃ 95 PA _o /SEG ₃ 96 PA _o /SEG ₃ 97 PA _o /SEG ₃ 98 PA _o /SEG ₃ 99 PA _o /SEG ₃ 90 PA _o /SEG ₃ 91 V _{oc} Applies voltage supplied to pull-down resistors			
73 P9 ₇ /SEG ₂₃ 74 P9 ₆ /SEG ₂₂ 75 P9 ₆ /SEG ₂₂ 76 P9 ₄ /SEG ₂₀ 8-bit output port with the same function as port 77 P9 ₃ /SEG ₁₆ 78 P9 ₂ /SEG ₁₆ 79 P9 ₇ /SEG ₁₇ 80 P9 ₆ /SEG ₁₇ 81 P8 ₇ /SEG ₁₇ 82 P8 ₆ /SEG ₁₈ 83 P8 ₆ /SEG ₁₃ 84 P8 ₄ /SEG ₁₃ 85 P8 ₆ /SEG ₁₄ 86 P8 ₂ /SEG ₁₆ 87 P8 ₃ /SEG ₁₇ 88 P8 ₆ /SEG ₁₆ 89 P8 ₄ /SEG ₁₆ 81 The high-breakdown-voltage P-channel opendrain 81 P8 ₇ /SEG ₆ 82 P8 ₆ /SEG ₇ 83 P8 ₆ /SEG ₈ 84 P8 ₆ /SEG ₈ 85 PA ₇ /SEG ₈ 86 PA ₇ /SEG ₈ 87 PA ₇ /SEG ₈ 88 P8 ₇ /SEG ₈ 89 PA ₇ /SEG ₈ 89 PA ₇ /SEG ₆ 80 PA ₇ /SEG ₆ 81 PA ₇ /SEG ₆ 82 PA ₇ /SEG ₆ 83 PA ₇ /SEG ₆ 84 PA ₇ /SEG ₆ 85 PA ₇ /SEG ₇ 85 PA ₇ /SEG ₈ 86 PA ₇ /SEG ₈ 87 PA ₇ /SEG ₉ 88 PA ₇ /SEG ₉ 99 PA ₇ /SEG ₉ 90 PA ₇ /SEG ₉ 91 PA ₇ /SEG ₉ 92 PA ₇ /SEG ₉ 94 PA ₇ /SEG ₉ 95 PA ₇ /SEG ₉ 96 PA ₇ /SEG ₉ 97 PA ₇ /SEG ₉ 98 PA ₇ /SEG ₉ 99 PA ₇ /S			
74 P9 ₆ /SEG ₂₂ 75 P9 ₅ /SEG ₂₁ 76 P9 ₄ /SEG ₂₀ 8-bit output port with the same function as port 77 P9 ₂ /SEG ₁₆ 78 P9 ₂ /SEG ₁₆ 79 P9 ₇ /SEG ₁₇ 80 P9 ₉ /SEG ₁₆ 81 P8 ₇ /SEG ₁₅ 82 P8 ₆ /SEG ₁₆ 83 P8 ₆ /SEG ₁₆ 84 P8 ₆ /SEG ₁₂ 85 P8 ₉ /SEG ₁₆ 86 P8 ₂ /SEG ₁₇ 86 P8 ₂ /SEG ₁₆ 87 P8 ₃ /SEG ₁₇ 88 P8 ₆ /SEG ₁₆ 89 P8 ₇ /SEG ₁₆ 81 P8 ₇ /SEG ₁₆ 82 P8 ₇ /SEG ₁₇ 83 P8 ₇ /SEG ₁₆ 84 P8 ₇ /SEG ₁₆ 85 P8 ₇ /SEG ₁₆ 86 P8 ₇ /SEG ₆ 87 P8 ₇ /SEG ₆ 88 P8 ₇ /SEG ₆ 89 PA ₇ /SEG ₆ 89 PA ₇ /SEG ₆ 81 V _{CC} 8-bit I/O port with the same function as ports 87 P8 ₇ /SEG ₇ 90 PA ₇ /SEG ₇ 91 V _{CC} 8-bit I/O port with the same function as ports 92 PA ₇ /SEG ₆ 93 PA ₇ /SEG ₆ 94 PA ₇ /SEG ₆ 95 PA ₇ /SEG ₇ 96 PA ₇ /SEG ₇ 97 PA ₇ /SEG ₇ 98 PA ₇ /SEG ₇ 99 PA ₇			- Laboratoria - Carallela - Ca
75 P9s/SEG21 76 P94/SEG20 8-bit output port with the same function as port 77 P93/SEG16 78 P92/SEG16 79 P91/SEG17 80 P9s/SEG16 81 P8s/SEG16 82 P8s/SEG16 83 P8s/SEG16 84 P8s/SEG16 85 P8s/SEG17 86 P8s/SEG17 86 P8s/SEG17 87 P8s/SEG17 88 P8s/SEG17 89 P8s/SEG17 80 P8s/SEG18 81 P8s/SEG16 82 P8s/SEG16 83 P8s/SEG17 84 P8s/SEG17 85 P8s/SEG17 86 P8s/SEG17 87 P8s/SEG6 88 P8s/SEG6 89 PAr/SEG6 89 PAr/SEG6 90 PAs/SEG6 91 Vcc Apply voltage of 4.0 to 5.5V to Vcc 92 PAs/SEG6 93 PAs/SEG6 94 PAs/SEG6 95 PAs/SEG6 96 PAs/SEG6 97 PAs/SEG6 98 PAs/SEG6 99 PAs/SEG6 99 PAs/SEG6 90 PAs/SEG6 91 PAs/SEG6 91 PAs/SEG6 92 PAs/SEG6 93 PAs/SEG6 94 PAs/SEG6 95 PAs/SEG6 96 PAs/SEG6 97 PAs/SEG7 98 PAs/SEG9 98 PAs/SEG9 99 PAS/SEG9			
76 P9 ₄ /SEG ₂₀ 77 P9 ₅ /SEG ₁₀ 78 P9 ₂ /SEG ₁₀ 79 P9 ₇ /SEG ₁₇ 80 P9 ₇ /SEG ₁₇ 81 P8 ₇ /SEG ₁₇ 82 P8 ₇ /SEG ₁₆ 83 P8 ₇ /SEG ₁₄ 83 P8 ₇ /SEG ₁₃ 84 P8 ₇ /SEG ₁₃ 85 P8 ₇ /SEG ₁₃ 86 P8 ₇ /SEG ₁₀ 87 P8 ₇ /SEG ₁₀ 87 P8 ₇ /SEG ₉ 88 P8 ₇ /SEG ₉ 89 PA ₇ /SEG ₉ 89 PA ₇ /SEG ₉ 90 PA ₈ /SEG ₈ 91 V _{cc} Apply voltage of 4.0 to 5.5V to V _{cc} 92 PA ₈ /SEG ₄ 93 PA ₇ /SEG ₄ 94 PA ₇ /SEG ₅ 95 PA ₇ /SEG ₆ 96 PA ₇ /SEG ₆ 97 PA ₇ /SEG ₇ 98 PA ₇ /SEG ₇ 99 PA ₇ /SEG ₈ 91 PA ₇ /SEG ₈ 92 PA ₇ /SEG ₉ 94 PA ₇ /SEG ₉ 95 PA ₇ /SEG ₉ 96 PA ₇ /SEG ₉ 97 PA ₇ /SEG ₉ 98 PA ₇ /SEG ₉ 99			
77 P9 ₃ /SEG ₁₀ 78 P9 ₂ /SEG ₁₈ 79 P9 ₇ /SEG ₁₇ 80 P9 ₉ /SEG ₁₇ 81 P8 ₇ /SEG ₁₈ 82 P8 ₉ /SEG ₁₄ 83 P8 ₉ /SEG ₁₄ 84 P8 ₄ /SEG ₁₂ 85 P8 ₉ /SEG ₁₃ 86 P8 ₂ /SEG ₁₀ 87 P8 ₇ /SEG ₉ 88 P8 ₇ /SEG ₉ 89 PA ₇ /SEG ₉ 89 PA ₇ /SEG ₉ 89 PA ₇ /SEG ₈ 89 PA ₇ /SEG ₈ 89 PA ₇ /SEG ₈ 89 PA ₇ /SEG ₉ 90 PA ₉ /SEG ₈ 89 PA ₇ /SEG ₉ 91 V _{cc} Apply voltage of 4.0 to 5.5V to V _{cc} 81 P2 ₄ -P2 ₇ 82 PA ₉ /SEG ₉ 83 PA ₇ /SEG ₉ 94 PA ₇ /SEG ₉ 95 PA ₇ /SEG ₉ 96 PA ₇ /SEG ₉ 97 PA ₇ /SEG ₉ 98 PA ₇ /SEG ₉ 99 PA ₇ /SEG ₉			8-bit output port with the same function as port P0
78 P9₂/SEG₁8 79 P9₁/SEG₁7 80 P99/SEG₁7 81 P8₀/SEG₁8 82 P8₀/SEG₁3 83 P8₀/SEG₁3 84 P8₃/SEG₁3 85 P8₃/SEG₁1 86 P8₂/SEG₁0 87 P8₂/SEG₁0 88 P8₂/SEG₁0 89 PA₂/SEG₂ 90 PA₂/SEG₂ 91 Vcc Apply voltage of 4.0 to 5.5V to Vcc 92 PA₃/SEG₃ 93 PA₂/SEG₃ 94 PA₃/SEG₃ 95 PA₂/SEG₃ 96 PA₂/SEG₃ 97 PA₂/SEG₃ 98 PA₂/SEG₃ 99 PA₂/SEG₃ 90 PA₂/SEG₃ 91 Vcc Apply voltage of 4.0 to 5.5V to Vcc 92 PA₂/SEG₃ 93 PA₂/SEG₃ 94 PA₃/SEG₃ 95 PA₂/SEG₃ 96 PA₂/SEG₃ 97 PA₂/SEG₃ 98 PA₂/SEG₃ 99 PA₂/SEG₃ 99 PA₂/SEG₃ 90 PA₂/SEG₃ 91 Vcc Apply voltage of 4.0 to 5.5V to Vcc 91 PA₂/SEG₃ 92 PA₂/SEG₃ 93 PA₂/SEG₃ 94 PA₃/SEG₃ 95 PA₂/SEG₃ 96 PA₂/SEG₃ 97 PA₂/SEG₃ 98 PA₂/SEG₃ 99 PA₂/SEG₃ 99 PA₂/SEG₃ 90 PA₂/SEG₃ 91 Vcc Applies voltage supplied to pull-down resistors			
79 P9_/SEG_17 80 P9_o/SEG_16 81 P8_r/SEG_15 82 P8_o/SEG_14 83 P8_o/SEG_13 84 P8_o/SEG_12 85 P8_o/SEG_10 86 P8_o/SEG_10 87 P8_o/SEG_10 88 P8_o/SEG_10 89 PA_r/SEG_2 90 PA_o/SEG_6 91 Vcc Apply voltage of 4.0 to 5.5V to Vcc 92 PA_o/SEG_3 93 PA_o/SEG_4 94 PA_o/SEG_3 95 PA_o/SEG_2 96 PA_o/SEG_2 97 PA_o/SEG_2 98 PA_o/SEG_3 99 PA_o/SEG_3 90 PA_o/SEG_3 91 PA_o/SEG_3 91 PA_o/SEG_3 92 PA_o/SEG_3 93 PA_o/SEG_3 94 PA_o/SEG_3 95 PA_o/SEG_3 96 PA_o/SEG_3 97 PA_o/SEG_0 98 Vc Applies voltage supplied to pull-down resistors			
80 P9 _o /SEG ₁₆ 81 P8 ₇ /SEG ₁₅ 82 P8 _o /SEG ₁₄ 83 P8 _o /SEG ₁₄ 8-bit I/O port with the same function as ports P2 ₄ -P2 ₇ C MOS compatible input level The high-breakdown-voltage P-channel opendrain P8 _o /SEG ₁₆ C MOS compatible input level The high-breakdown-voltage P-channel opendrain P8 _o /SEG ₁₆ C MOS compatible input level P8 _o /SEG ₁₆ PA _o /SEG ₁₆ PA _o /SEG ₁₇ PA _o /SEG ₁₈ P2 _o -P2 _o -P2 _o PA _o /SEG ₁₈ P2 _o -P2 _o -P2 _o PA _o /SEG ₁₈ P2 _o -P2 _o -P2 _o PA _o /SEG ₁₈ PA _o /SEG ₁₉ PA _o /SE			
81 P8 ₇ /SEG ₁₅ 82 P8 ₆ /SEG ₁₄ 83 P8 ₆ /SEG ₁₃ 84 P8 ₆ /SEG ₁₂ 85 P8 ₃ /SEG ₁₂ 86 P8 ₃ /SEG ₁₀ 87 P8 ₃ /SEG ₃ 88 P8 ₆ /SEG ₆ 89 PA ₇ /SEG ₃ 90 PA ₈ /SEG ₆ 91 V _{cc} Apply voltage of 4.0 to 5.5V to V _{cc} 92 PA ₈ /SEG ₆ 93 PA ₄ /SEG ₃ 94 PA ₃ /SEG ₃ 95 PA ₄ /SEG ₃ 96 PA ₄ /SEG ₂ 97 PA ₆ /SEG ₃ 98 PA ₆ /SEG ₃ 99 PA ₆ /SEG ₃ 90 PA ₆ /SEG ₃ 91 PA ₆ /SEG ₃ 95 PA ₆ /SEG ₃ 96 PA ₆ /SEG ₃ 97 PA ₆ /SEG ₃ 98 PA ₆ /SEG ₃ 99 PA ₆ /SEG ₃ 90 PA ₆ /SEG ₃ 91 PA ₆ /SEG ₃ 92 <t< td=""><td></td><td></td><td></td></t<>			
82 P8 _o /SEG ₁₄ 83 P8 _o /SEG ₁₃ 84 P8 _o /SEG ₁₂ 85 P8 _o /SEG ₁₂ 86 P8 _o /SEG ₁₁ 86 P8 _o /SEG ₁₁ 87 P8 _o /SEG ₁₀ 88 P8 _o /SEG ₁₀ 89 PA _o /SEG ₀ 89 PA _o /SEG ₀ 89 PA _o /SEG ₀ 80 PA _o /SEG ₀ 81 V _{cc} 8-bit I/O port with the same function as ports product of a compatible input level 80 P8 _o /SEG ₀ 81 P8 _o /SEG ₀ 82 PA _o /SEG ₀ 83 PA _o /SEG ₀ 84 PA _o /SEG ₀ 85 PA _o /SEG ₀ 86 PA _o /SEG ₀ 87 PA _o /SEG ₀ 88 Poit I/O port with the same function as ports product product of a compatible input level 86 PA _o /SEG ₀ 87 PA _o /SEG ₀ 88 Poit I/O port with the same function as ports product prod	-		
83 P8 _o /SEG ₁₃ 84 P8 _d /SEG ₁₂ 85 P8 _d /SEG ₁₂ 86 P8 _o /SEG ₁₃ 87 P8 _o /SEG ₁₃ 88 P8 _o /SEG ₁₄ 89 P8 _o /SEG ₃ 89 P8 _o /SEG ₃ 89 PA _o /SEG ₃ 89 PA _o /SEG ₅ 80 PA _o /SEG ₆ 81 V _{cc} 82 PA _o /SEG ₆ 83 PA _o /SEG ₆ 84 PA _o /SEG ₆ 85 PA _o /SEG ₆ 86 PA _o /SEG ₆ 87 PA _o /SEG ₆ 88 PA _o /SEG ₆ 89 PA _o /SEG ₆ 80 PA _o /SEG ₆ 81 PA _o /SEG ₆ 82 PA _o /SEG ₆ 83 PA _o /SEG ₆ 84 PA _o /SEG ₆ 85 PA _o /SEG ₆ 86 PA _o /SEG ₆ 86 PA _o /SEG ₆ 87 PA _o /SEG ₆ 88 PA _o /SEG ₆ 99 PA _o /SEG ₆ 99 PA _o /SEG ₆ 90 PA _o /SEG ₆ 90 PA _o /SEG ₆ 91 PA _o /SEG ₆ 92 PA _o /SEG ₆ 93 PA _o /SEG ₆ 94 PA _o /SEG ₆ 95 PA _o /SEG ₆ 96 PA _o /SEG ₆ 97 PA _o /SEG ₆ 98 PA _o /SEG ₆ 99 PA _o /SEG ₆	1	to the second of the second	
84			8-bit I/O port with the same function as ports
85 P8 ₉ /SEG ₁₁ C MOS compatible input level 86 P8 ₂ /SEG ₁₀ The high-breakdown-voltage P-channel opendrain 88 P8 ₉ /SEG ₉ 89 PA ₇ /SEG ₇ 90 PA ₉ /SEG ₈ 91 V _{CC} Apply voltage of 4.0 to 5.5V to V _{CC} 92 PA ₉ /SEG ₉ 8-bit I/O port with the same function as ports 93 PA ₇ /SEG ₄ P2 ₄ -P2 ₇ 94 PA ₇ /SEG ₃ C MOS compatible input level 95 PA ₇ /SEG ₂ The high-breakdown-voltage P-channel opendrain 96 PA ₇ /SEG ₁ drain 97 PA ₉ /SEG ₀ Applies voltage supplied to pull-down resistors			
86 P8 ₂ /SEG ₁₀ The high-breakdown-voltage P-channel opendrain 88 P8 ₀ /SEG ₉ 89 PA ₂ /SEG ₇ 90 PA ₂ /SEG ₆ Apply voltage of 4.0 to 5.5V to V _{cc} 92 PA ₃ /SEG ₃ 8-bit I/O port with the same function as ports 93 PA ₄ /SEG ₃ P2 ₄ -P2 ₇ 94 PA ₃ /SEG ₃ C MOS compatible input level 95 PA ₄ /SEG ₂ The high-breakdown-voltage P-channel opendrain 96 PA ₁ /SEG ₁ Applies voltage supplied to pull-down resistors			
87 P8,/SEG ₉ drain 88 P8 _o /SEG ₉ 89 PA _r /SEG ₇ 90 PA _o /SEG ₆ 91 V _{cc} Apply voltage of 4.0 to 5.5V to V _{cc} 92 PA _o /SEG ₉ 93 PA _o /SEG ₄ P2 _o -P2 _o 94 PA _o /SEG ₃ C MOS compatible input level 95 PA _o /SEG ₂ The high-breakdown-voltage P-channel opendrain 96 PA _o /SEG ₁ 97 PA _o /SEG ₂ Applies voltage supplied to pull-down resistors			
88 P8 _o /SEG _e 89 PA _r /SEG ₇ 90 PA _o /SEG ₆ 91 V _{cc} Apply voltage of 4.0 to 5.5V to V _{cc} 92 PA _o /SEG ₆ 8-bit I/O port with the same function as ports 93 PA _o /SEG ₄ P2 _o -P2 _r 94 PA _o /SEG ₃ C MOS compatible input level 95 PA _o /SEG ₂ The high-breakdown-voltage P-channel opendrain 96 PA _o /SEG ₁ 97 PA _o /SEG ₂ Applies voltage supplied to pull-down resistors			
89 PA ₇ /SEG ₇ 90 PA ₆ /SEG ₆ 91 V _{cc} Apply voltage of 4.0 to 5.5V to V _{cc} 92 PA ₆ /SEG ₆ 8-bit I/O port with the same function as ports 93 PA ₇ /SEG ₄ P2 ₄ -P2 ₇ 94 PA ₃ /SEG ₂ C MOS compatible input level 95 PA ₂ /SEG ₂ The high-breakdown-voltage P-channel opendrain 96 PA ₇ /SEG ₁ drain 97 PA ₇ /SEG ₂ Applies voltage supplied to pull-down resistors			
90 PA _o /SEG ₆ 91 V _{cc} Apply voltage of 4.0 to 5.5V to V _{cc} 92 PA _o /SEG ₆ 8-bit I/O port with the same function as ports 93 PA _o /SEG ₄ P2 _o -P2 _o 94 PA _o /SEG ₃ C MOS compatible input level 95 PA _o /SEG ₂ The high-breakdown-voltage P-channel opendrain 97 PA _o /SEG ₀ Applies voltage supplied to pull-down resistors			
91 V _{cc} Apply voltage of 4.0 to 5.5V to V _{cc} 92 PA _b /SEG _b 8-bit I/O port with the same function as ports 93 PA _c /SEG ₄ P2 ₄ -P2 ₇ 94 PA _c /SEG ₃ C MOS compatible input level 95 PA _c /SEG ₂ The high-breakdown-voltage P-channel opendrain 97 PA _c /SEG ₀ Applies voltage supplied to pull-down resistors			
92 PA ₈ /SEG ₈ 8-bit I/O port with the same function as ports 93 PA ₄ /SEG ₄ P2 ₄ -P2 ₇ 94 PA ₉ /SEG ₃ C MOS compatible input level 95 PA ₂ /SEG ₂ The high-breakdown-voltage P-channel opendrain 97 PA ₀ /SEG ₀ Applies voltage supplied to pull-down resistors			A 1 W (404) 5 5 14 14
93 PA,/SEG4 P2,-P2, 94 PA,/SEG3 C MOS compatible input level 95 PA,/SEG2 The high-breakdown-voltage P-channel opendrain 97 PA,/SEG0 Applies voltage supplied to pull-down resistors	-		
94 PAJSEG ₃ C MOS compatible input level 95 PA ₂ /SEG ₂ The high-breakdown-voltage P-channel open- 96 PA ₃ /SEG ₃ drain 97 PA ₃ /SEG ₃ Applies voltage supplied to pull-down resistors			
95 PA ₂ /SEG ₂ The high-breakdown-voltage P-channel open- 96 PA ₁ /SEG ₁ drain 97 PA ₀ /SEG ₀ Applies voltage supplied to pull-down resistors			
96 PA,/SEG, drain 97 PA _o /SEG _o Applies voltage supplied to pull-down resistors		(
97 PA/SEG。 Applies voltage supplied to pull-down resistors			
Applies voltage supplied to pull-down resistors		·	drain
	97	PA₀/SEG₀	
ports 1 0, 1 1, 1 20-1 23, 1 0 and 1 3	98	VEE	Applies voltage supplied to pull-down resistors of ports P0, P1, P2 ₀ -P2 ₃ , P3 and P9
99 AV _{ss} GND input pin for A-D converter and D-A converted AV _{ss} to V _{ss}	99	AVss	GND input pin for A-D converter and D-A converter Connect AV $_{\rm ss}$ to V $_{\rm ss}$
100 V _{REF} Reference voltage input pin for A-D converter and D-A converter	100	Vaer	Reference voltage input pin for A-D converter and D-A converter

